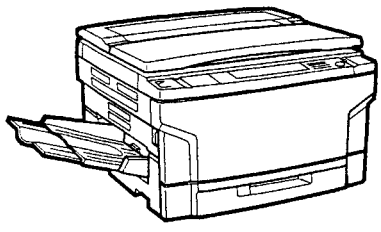
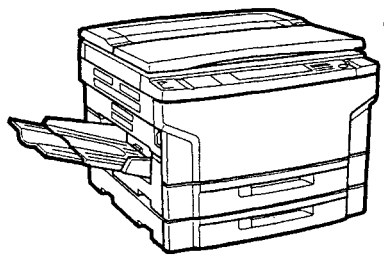
SHARP CIRCUIT DIAGRAM

CODE : 00ZSF2118CD-M



SF-2116

MODEL SF-2116/2118



SF-2118

Options
- Paper tray (SF-UB15)
- Multi paper feed unit (SF-MF15)
- Two-step paper feed unit (SF-CM15)
- Personal counter (SF-71A/71B)
- 10-bin sorter (SF-S17)※
- Reverse document feeder (SF-A15)※

※ For the options, refer to their service manuals.

オプション
- 用紙トレイ(SF-UB15)
- マルチ給紙ユニット(SF-MF15)
- 給紙ユニット(SF-CM15)
- パーソナルカウンター(SF-71A/71B)
- 10 ビンソーター(SF-S17)※
- 原稿自動送り装置(SF-A15)※

※印オプションに関しては、専用サービスマニュアルを参照下さい。

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安全性・信頼性確保のため部品は、必ず正規のものをご使用下さい。
⚠印の部品は、安全上重要な部品です。交換をする時は、安全および性能維持のため必ず指定の部品をご使用下さい。

Parts marked with "⚠" is important for maintaining the safety of the set. Be sure to replace these parts with specified ones for maintaining the safety and performance of the set.

SHARP CORPORATION

This document has been published to be used for after sales service only.
The contents are subject to change without notice.

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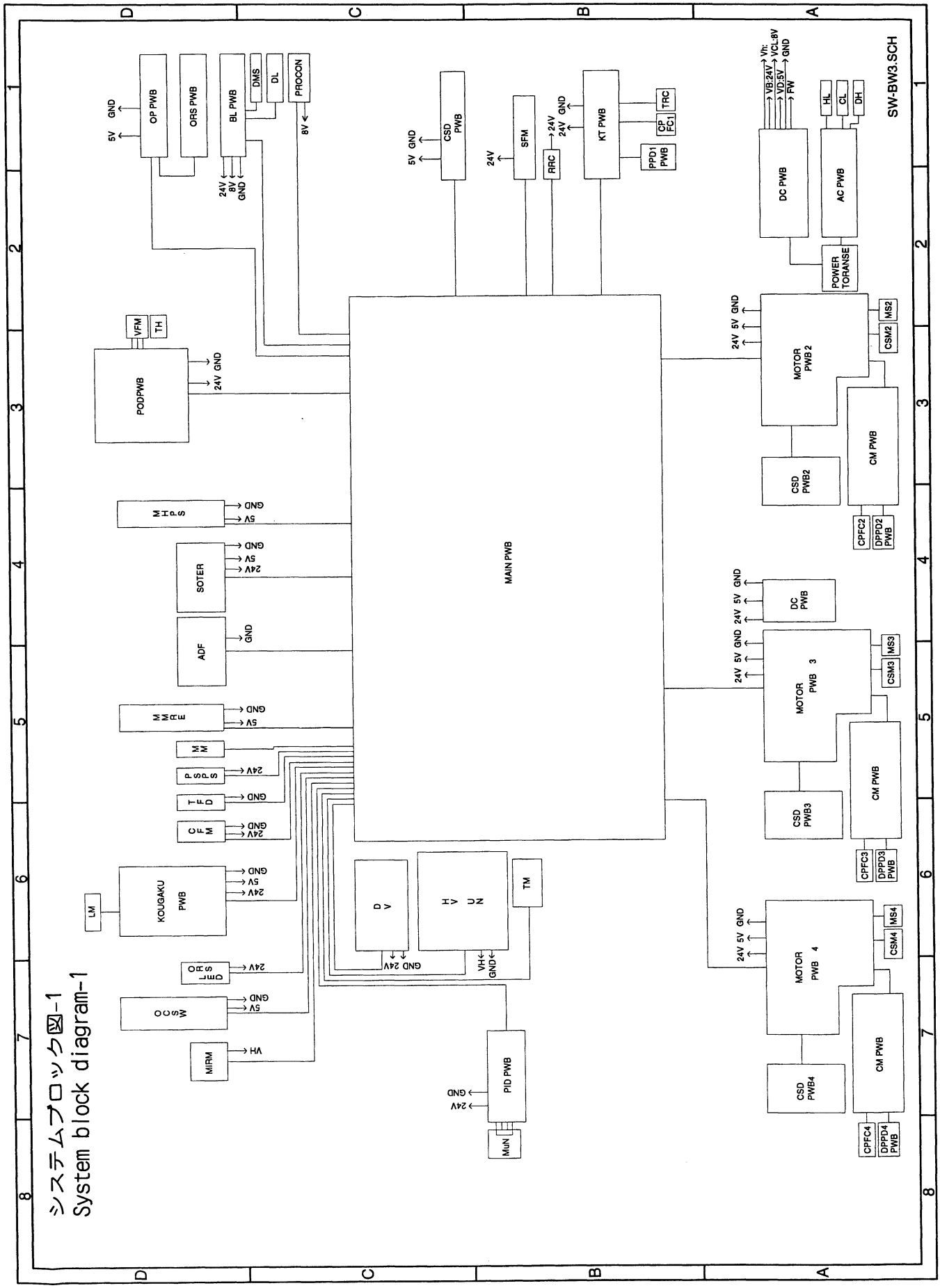
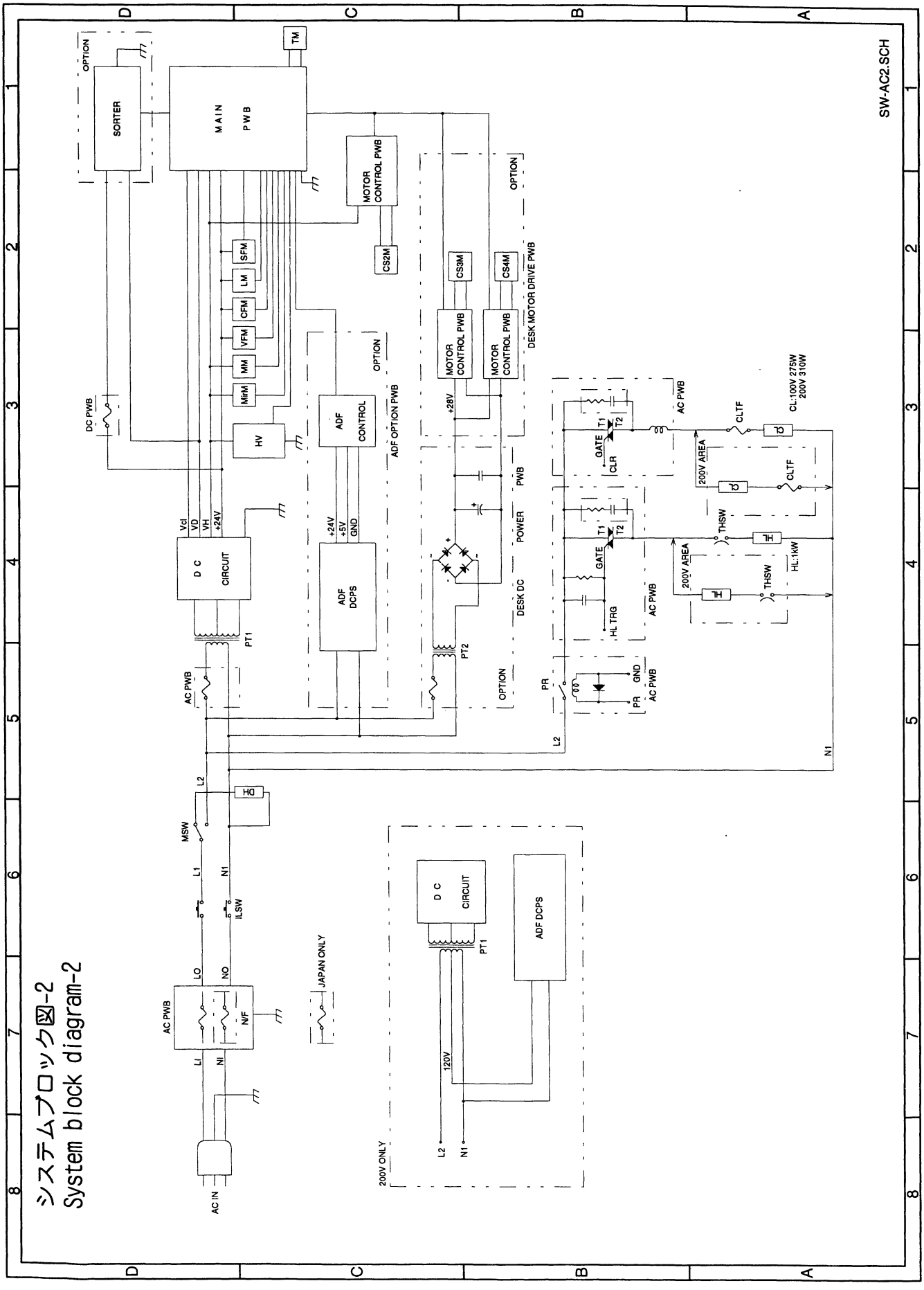
(Model with heater:Japan only)


SW-BW3.SCH
MAIN PWB
OP PWB
ORS PWB
BL PWB
DMS
DL
PROCON
CSD PWB
SFM
RRC
KT PWB
OP FC1
TRC
PPD1 PWB
DC PWB
AC PWB
HL
CL
DH
POWER TORANSE
MOTOR PWB 2
MS2
CSM2
CSD PWB2
CM PWB
CPFC2
DPPD2 PWB
PODPWB
VFM
TH
MHPS
SOTER
ADF
MMRE
MM
PPPS
TFD
CFM
KOUGAKU PWB
LM
ORES D
OCSW
MIRM
DV
HVUN
TM
PID PWB
MuN
MOTOR PWB 3
MS3
CSM3
CSD PWB3
CPFC3
DPPD3 PWB
MOTOR PWB 4
MS4
CSM4
CSD PWB4
CPFC4
DPPD4 PWB
システムブロック図-1
System block diagram-1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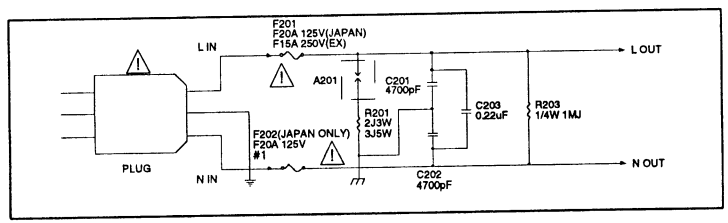
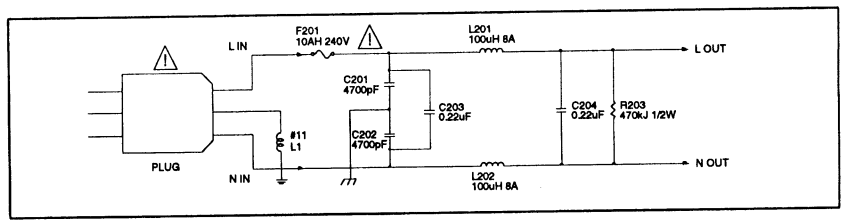
System block diagram-2
SW-AC2.SCH
OPTION
SORTER
MAIN PWB
TM
MOTOR CONTROL PWB
CS2M
CS3M
CS4M
SFM
LM
CFM
VFM
MM
MRM
HV
DC PWB
ADF CONTROL
ADF OPTION PWB
DESK MOTOR DRIVE PWB
+28V
Vcl
VD
VH
+24V
+5V
GND
D C CIRCUIT
ADF DCPS
PT1
PT2
POWER PWB
DESK DC
AC PWB
PR
L2
N1
GATE
CLR
T1
T2
HL TRG
200V AREA
CLTF
THSW
HL:1kW
CL:100V 275W
200V 310W
MSW
L1
ILSW
LO
NO
N/F
LI
NI
AC IN
JAPAN ONLY
120V
200V ONLY

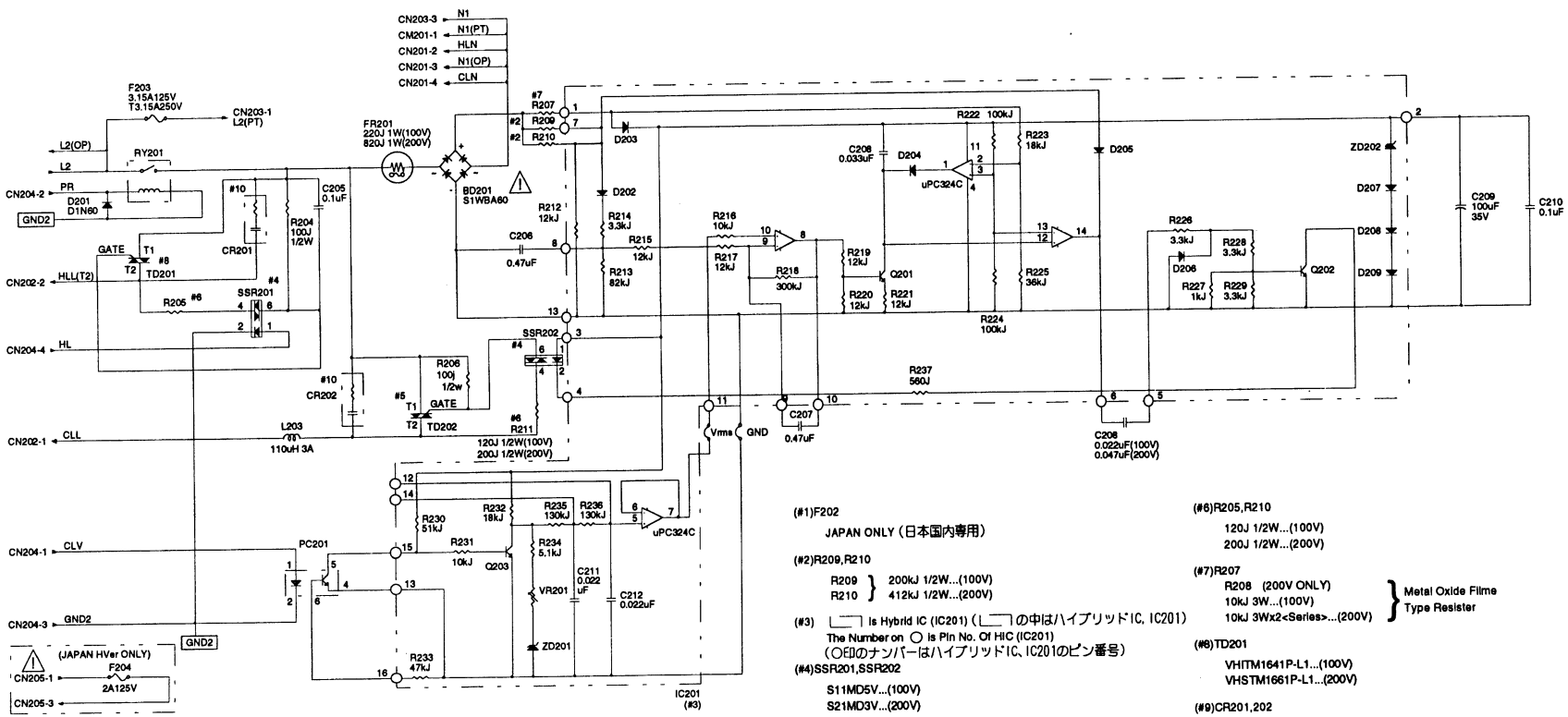
[1] AC電源回路
[1] AC Power supply circuit



100V AREA



200V AREA



(#1)F202
JAPAN ONLY（日本国内専用）

(#2)R209,R210
R209 } 200kJ 1/2W...(100V)
R210 } 412kJ 1/2W...(200V)

(#3) ┌─┐ is Hybrid IC (IC201) (┌─┐の中はハイブリッドIC, IC201)
The Number on ○ is Pin No. Of HIC (IC201)
(○印のナンバーはハイブリッドIC、IC201のピン番号)

(#4)SSR201,SSR202
S11MD5V...(100V)
S21MD3V...(200V)

(#5)TD202
VHSTM1661S-LF...(100V/200V)

(#6)R205,R210
120J 1/2W...(100V)
200J 1/2W...(200V)

(#7)R207
R208 (200V ONLY)
10kJ 3W...(100V)
10kJ 3Wx2<Series>...(200V)
} Metal Oxide Filme Type Resister

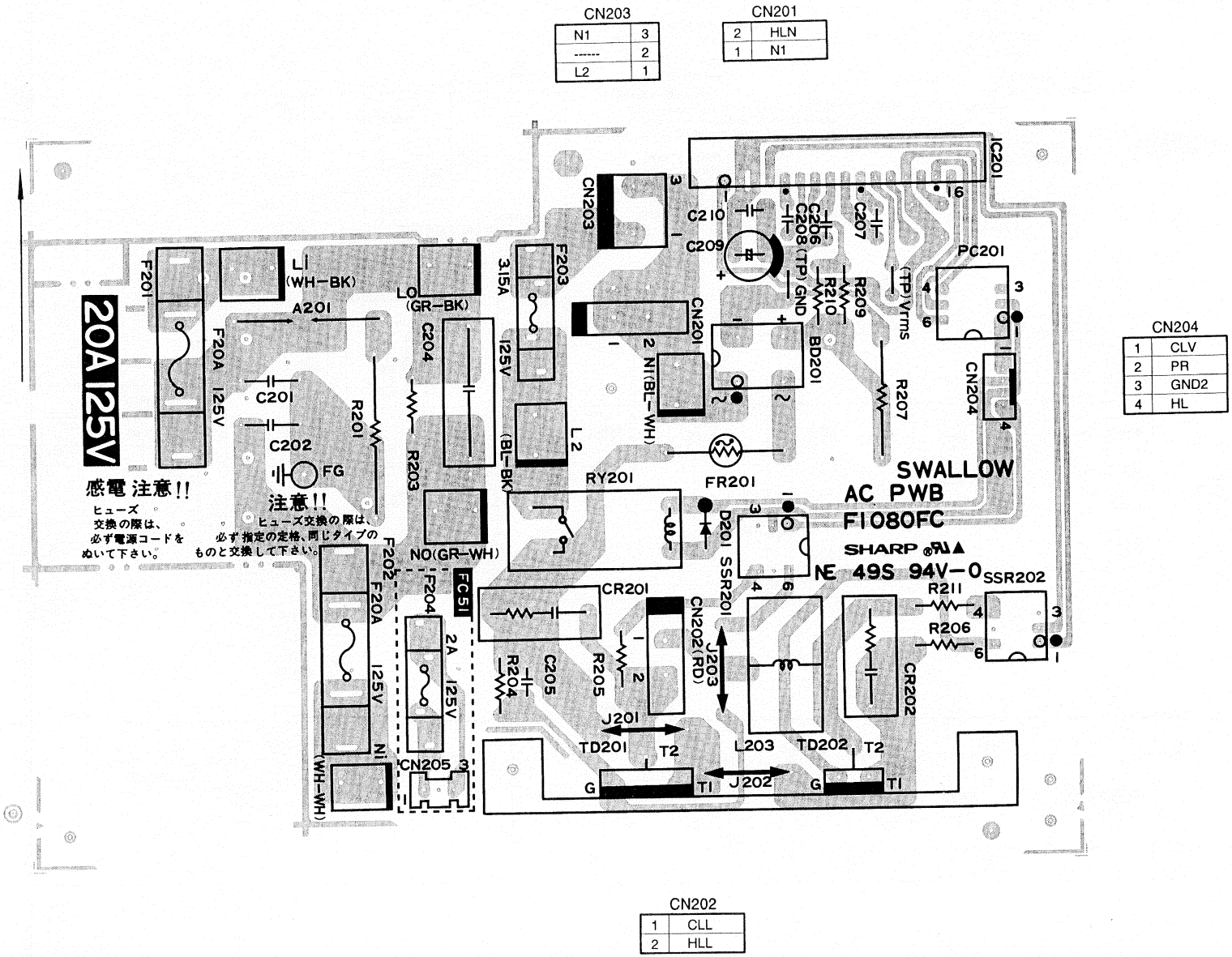
(#8)TD201
VHITM1641P-L1...(100V)
VHSTM1661P-L1...(200V)

(#9)CR201,202
ECQJ0186X

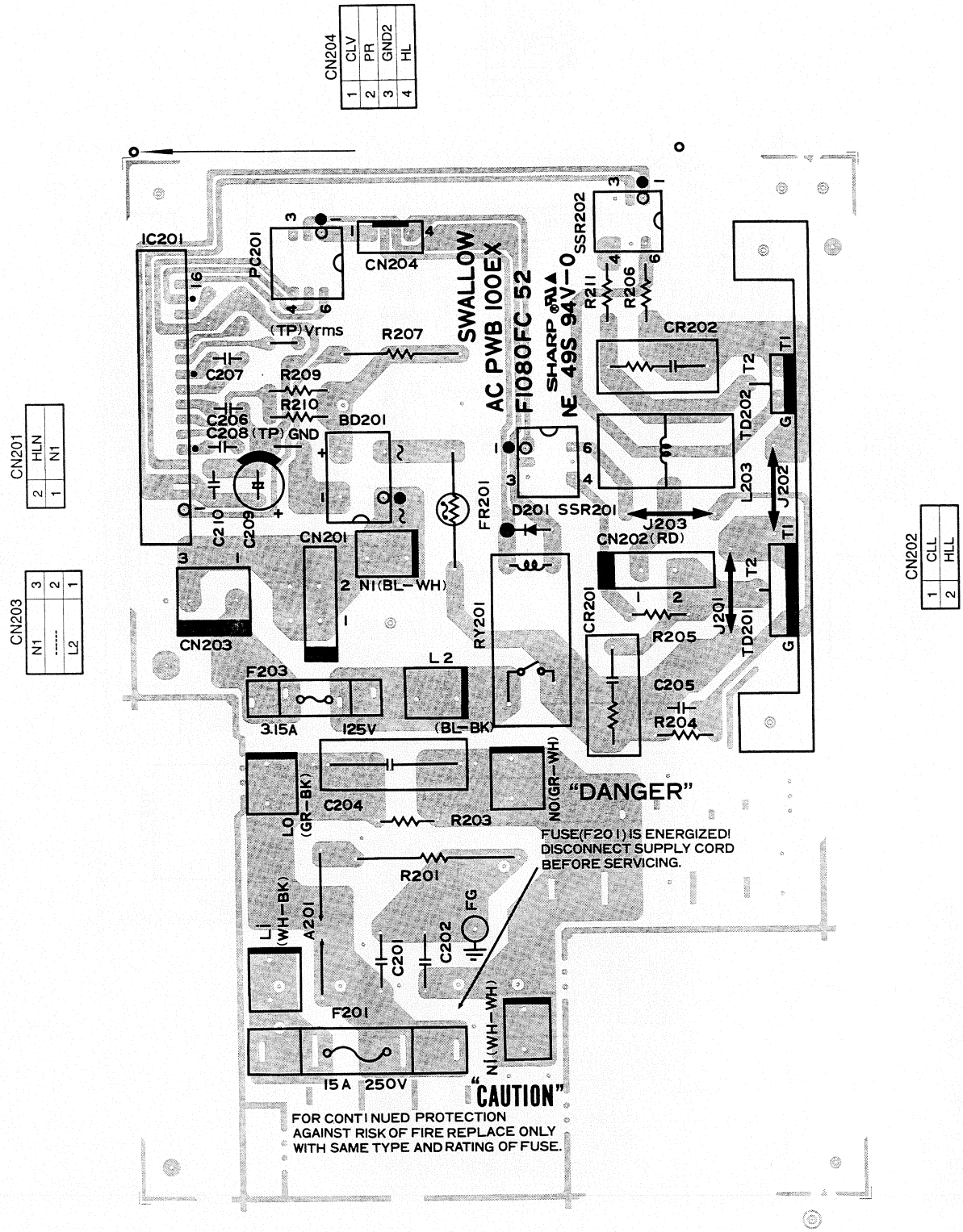
(#10)F204
QFS-2002WCZZ...(JAPAN HVer ONLY)（日本国内ヒーターモデル専用）

(#11)L1
RCILF0045FCZZ

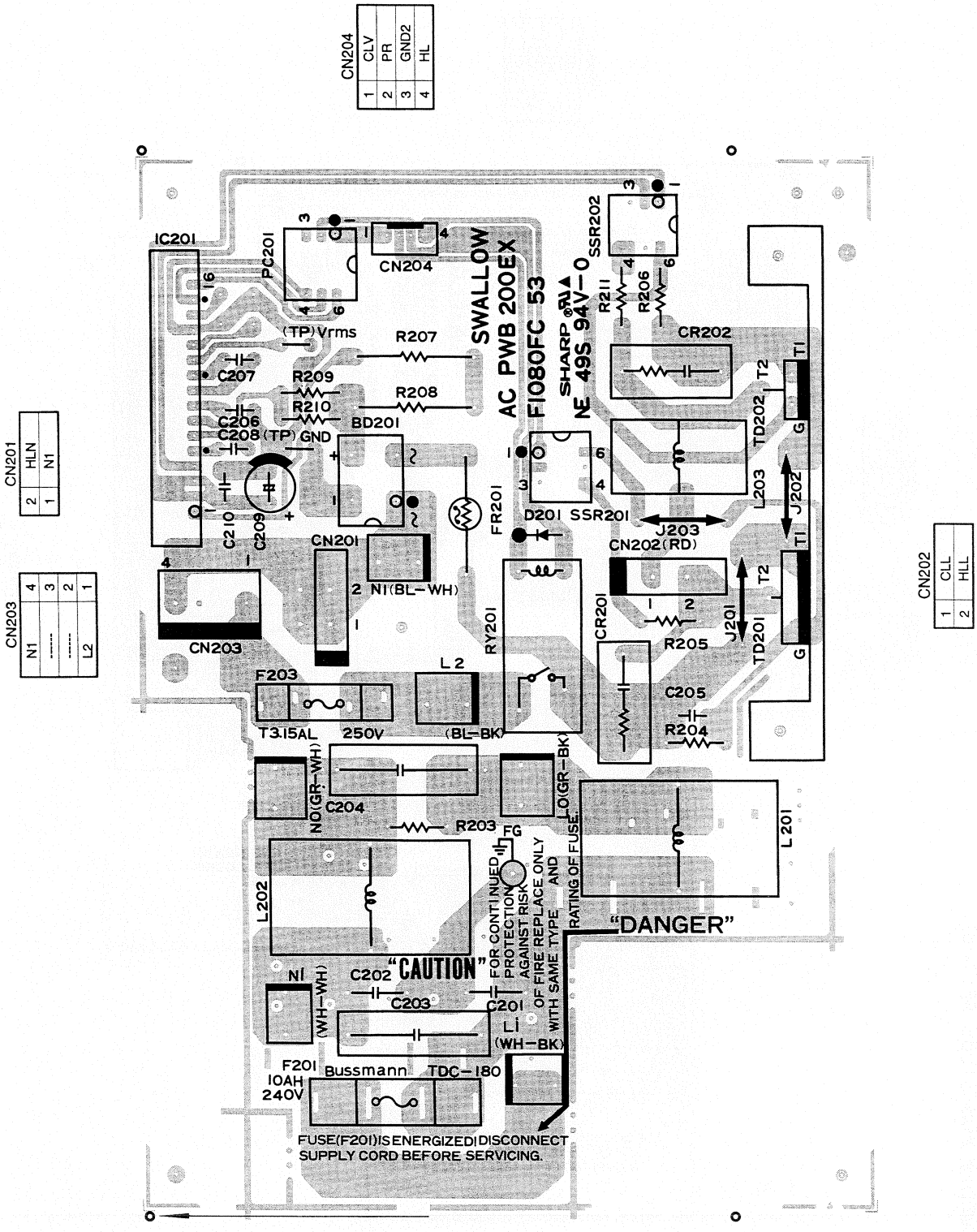
AC電源基板(国内向)
AC power supply PWB (Japan only)



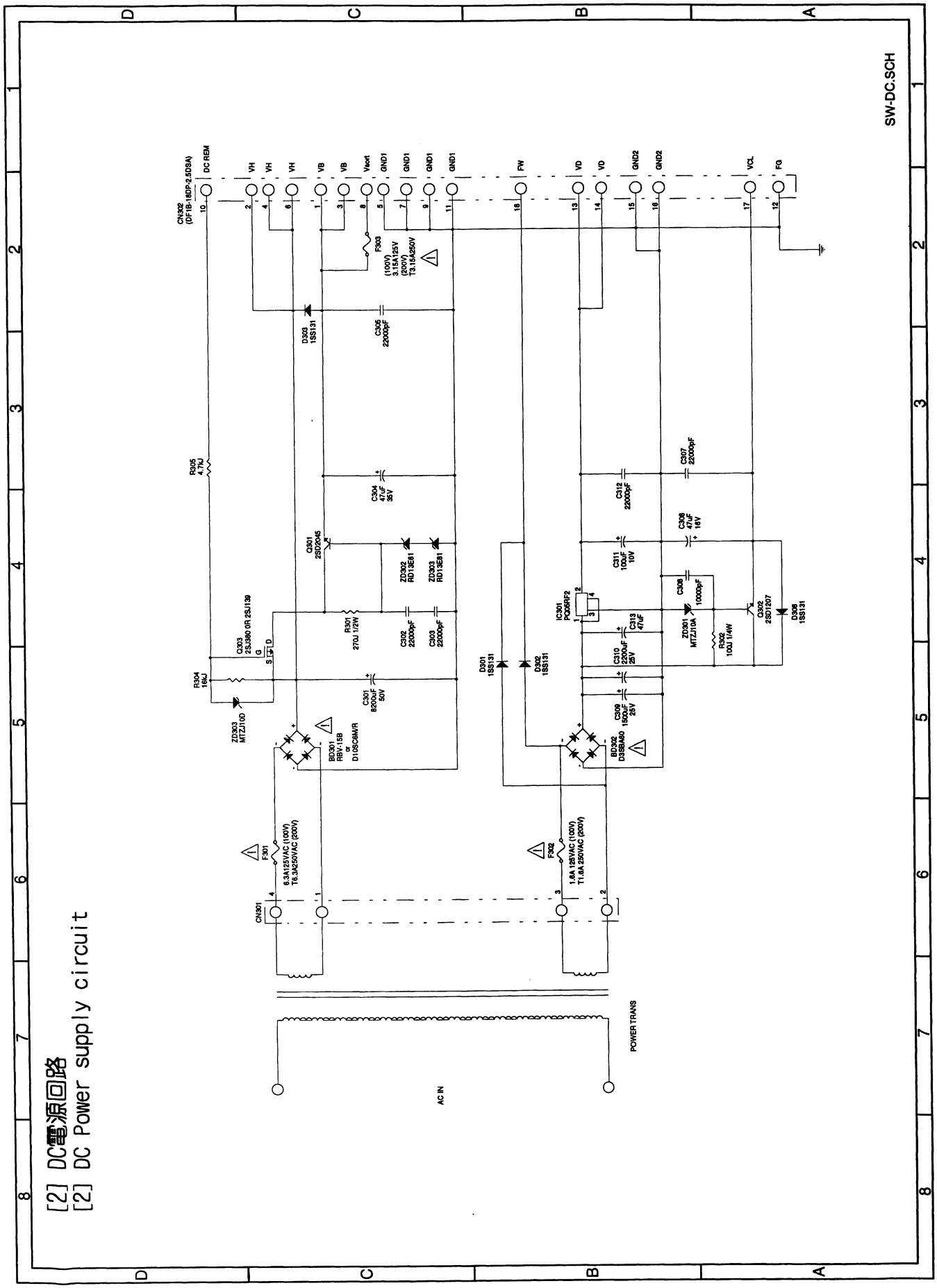
AC電源基板(100V系)
AC power supply PWB (100V series)



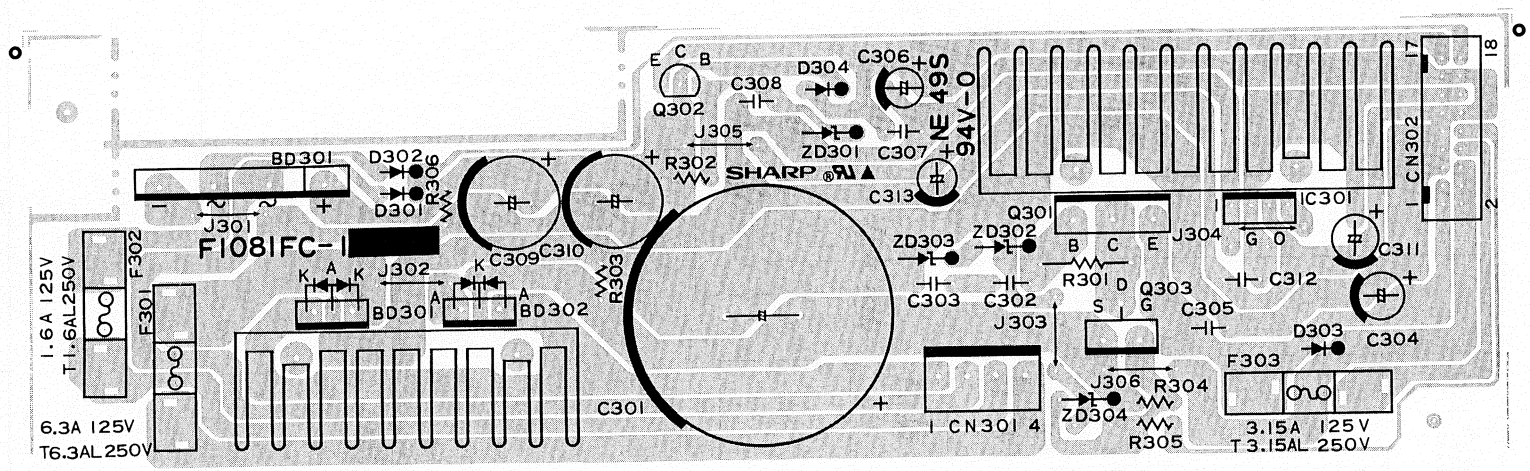
AC電源基板(200V系)
AC power supply PWB (200V series)



[2] DC電源回路
[2] DC Power supply circuit



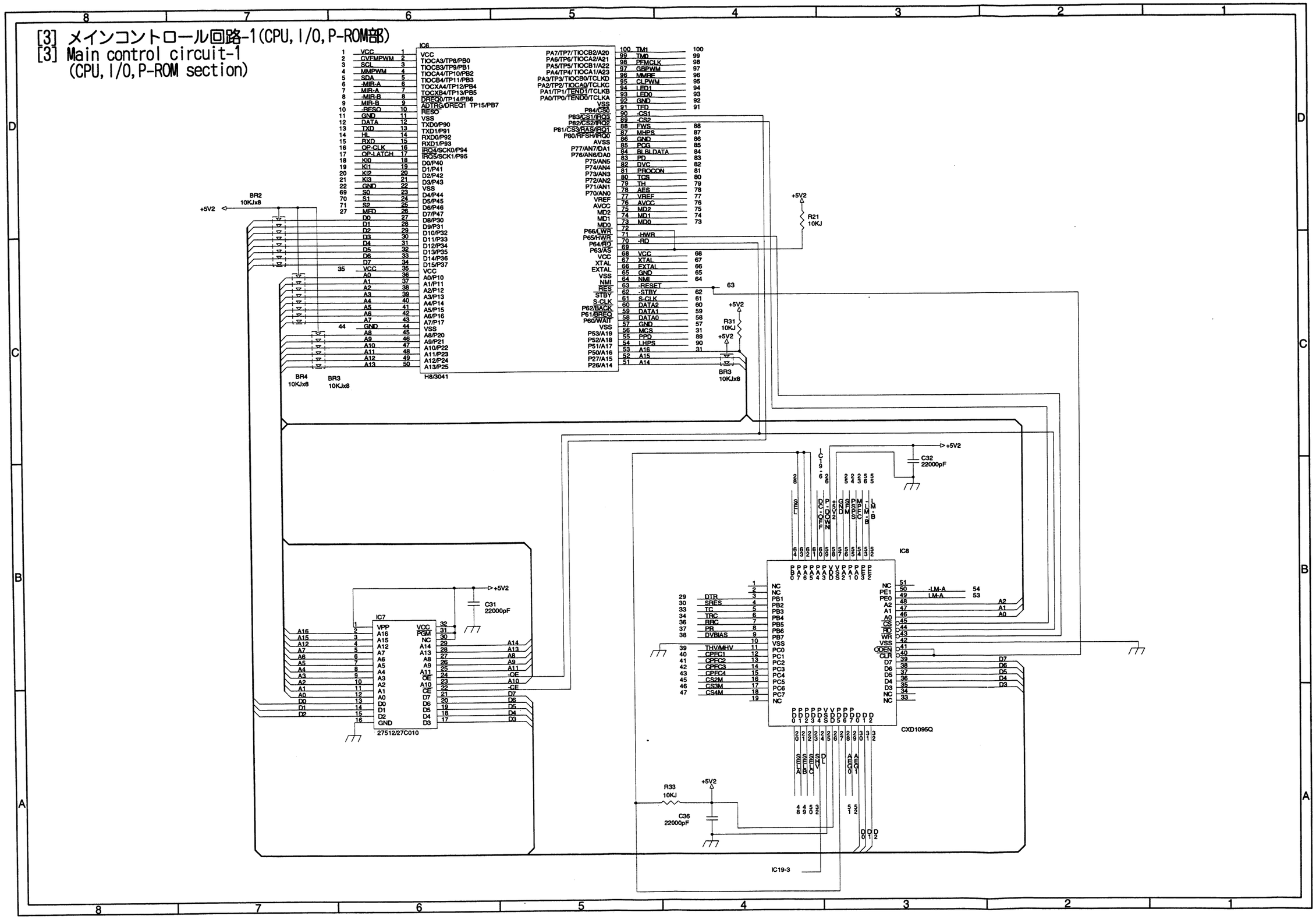
DC電源基板
DC power supply PWB



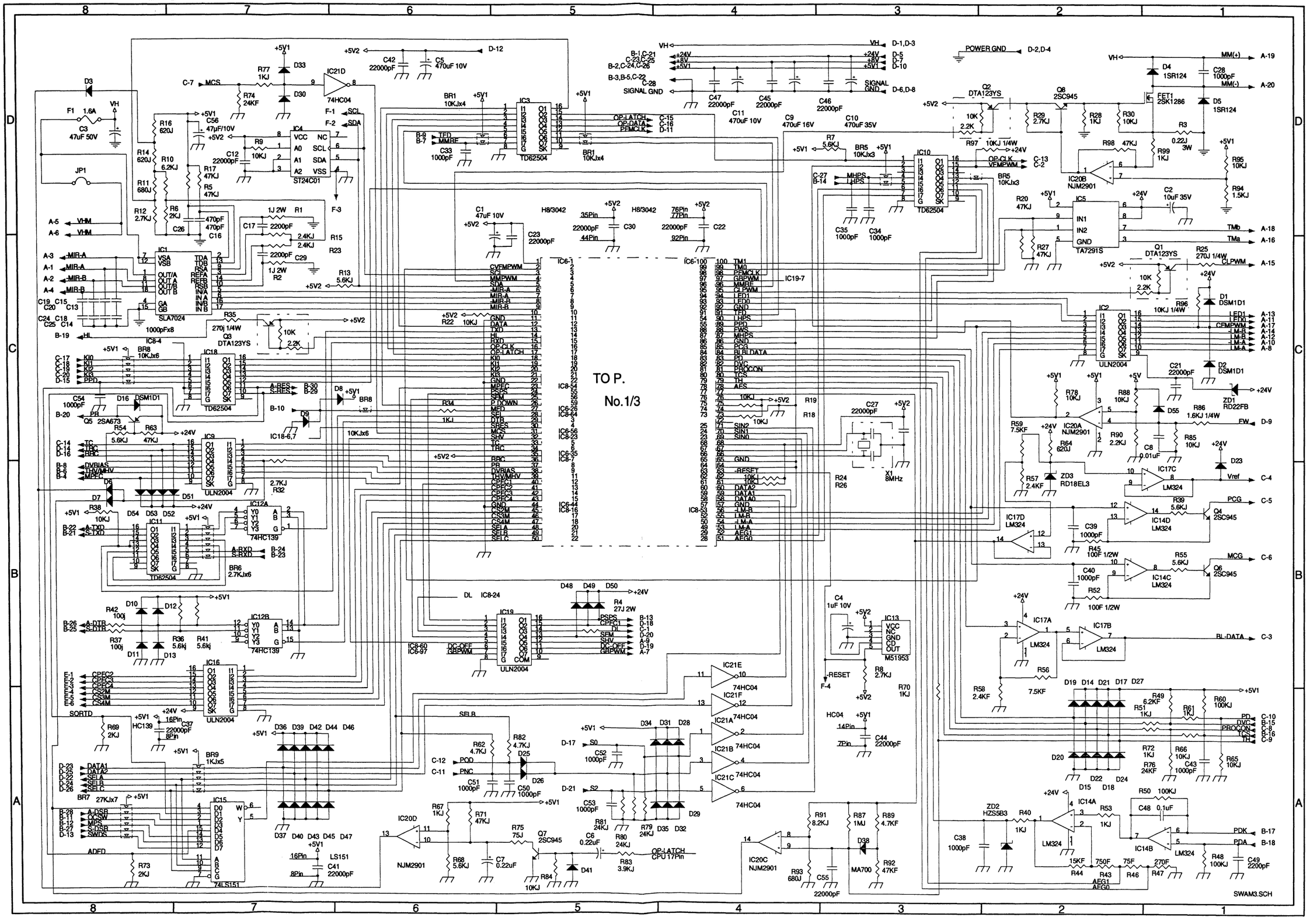
CN302

VCL	17	18	FW
GND2	15	16	GND2
VD	13	14	VD
GND1	11	12	FG
GND1	9	10	DC REM
GND1	7	8	Vsort
GND1	5	6	VH
VB	3	4	VH
VB	1	2	VH

[3] メインコントロール回路-1(CPU,I/O,P-ROM部)
[3] Main control circuit-1 (CPU,I/O,P-ROM section)



メインコントロール回路-2 Main control circuit-2



メインコントロール基板
Main control PWB

CN-C

SIGNAL	No.	No.	SIGNAL
VFM/PWM	2	1	DL
Vref	4	3	BL-DATA
MCG	6	5	KCS
PHOCON	8	7	KCG
PD	10	9	TH
POD	12	11	PNC
TC	14	13	OP-CLK
OP-DATA	16	15	OP-LATCH
KI1	18	17	KI0
KI3	20	19	KI2
SIGNAL GND	22	21	24V
5V1	24	23	VCC
SIGNAL GND	26	25	VCL
	28	27	M+PS

CN-B

SIGNAL	No	No	SIGNAL
5V1	2	1	24V
MPFC	4	3	SIGNAL GND
THM/AHV	6	5	SIGNAL GND
DV/BIAS	8	7	MMRE
MFD	10	9	TFD
MPS	12	11	OCSW
LHPS	14	13	PSPS
TCS	16	15	DVC
PDA	18	17	DVK
PDB	20	19	HL
A-TXD	22	21	S-TXD
-A-RXD	24	23	-S-RXD
-A-DTR	26	25	-S-DTR
-A-DSR	28	27	-S-DSR
ARES	30	29	SRES

CN-A

SIGNAL	No	No	SIGNAL
-MIR-B	2	1	MIR-A
MIR-B	4	3	-MIR-A
VHM-B	6	5	VHM
LM-A	8	7	GBPWM
-LM-A	10	9	SHV
LM-B	12	11	LED0
-LM-B	14	13	LED1
TMa	16	15	CFMPWM
TMb	18	17	CFMPWM
MM(-)	20	19	MM(+)

CN-D

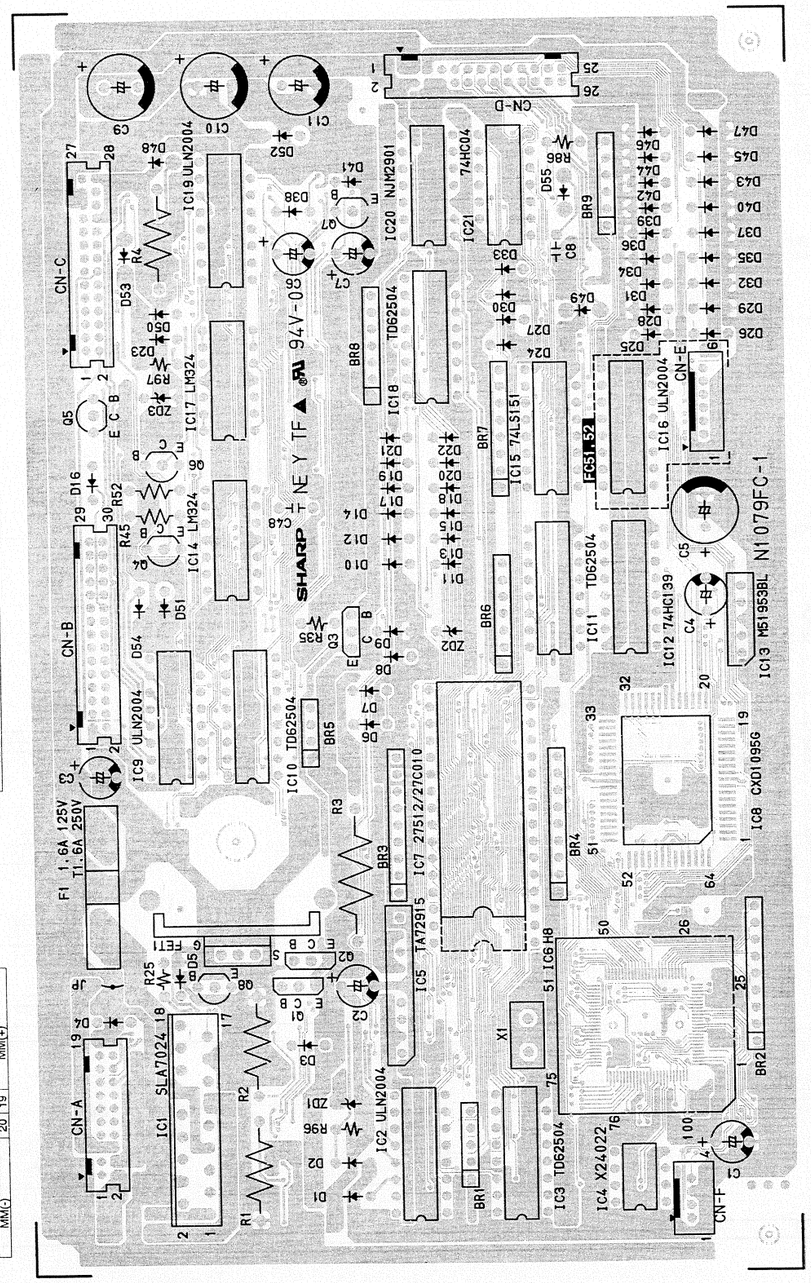
SIGNAL	No	No	SIGNAL
SIGNAL GND	2	1	VH
POWER GND	4	3	VH
POWER GND	6	5	24V
SIGNAL GND	8	7	VCL
SIGNAL GND	10	9	PWM
5V1	12	11	PFMCLK
5V2	14	13	SWDS
PFC	16	15	PPD
PFC	18	17	S0
CPFC1	20	19	DC-OFF
SFM	22	21	S2
SELA	24	23	DATA1
SELB	26	25	DATA2
SELC			

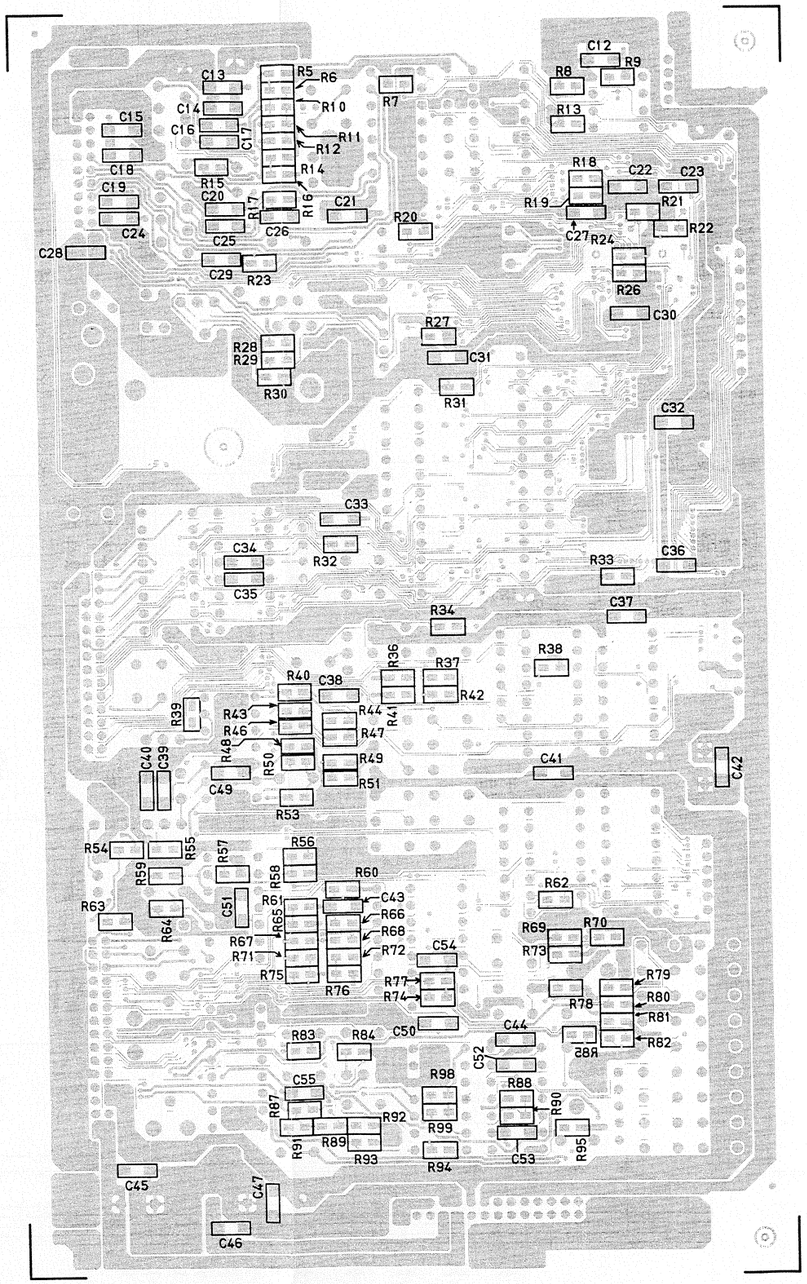
CN-E
(SF-2118/1018のみ)

No	SIGNAL
1	CPFC2
2	CPFC3
3	CPFC4
4	CS2M
5	CS3M
6	CS4M

CN-F

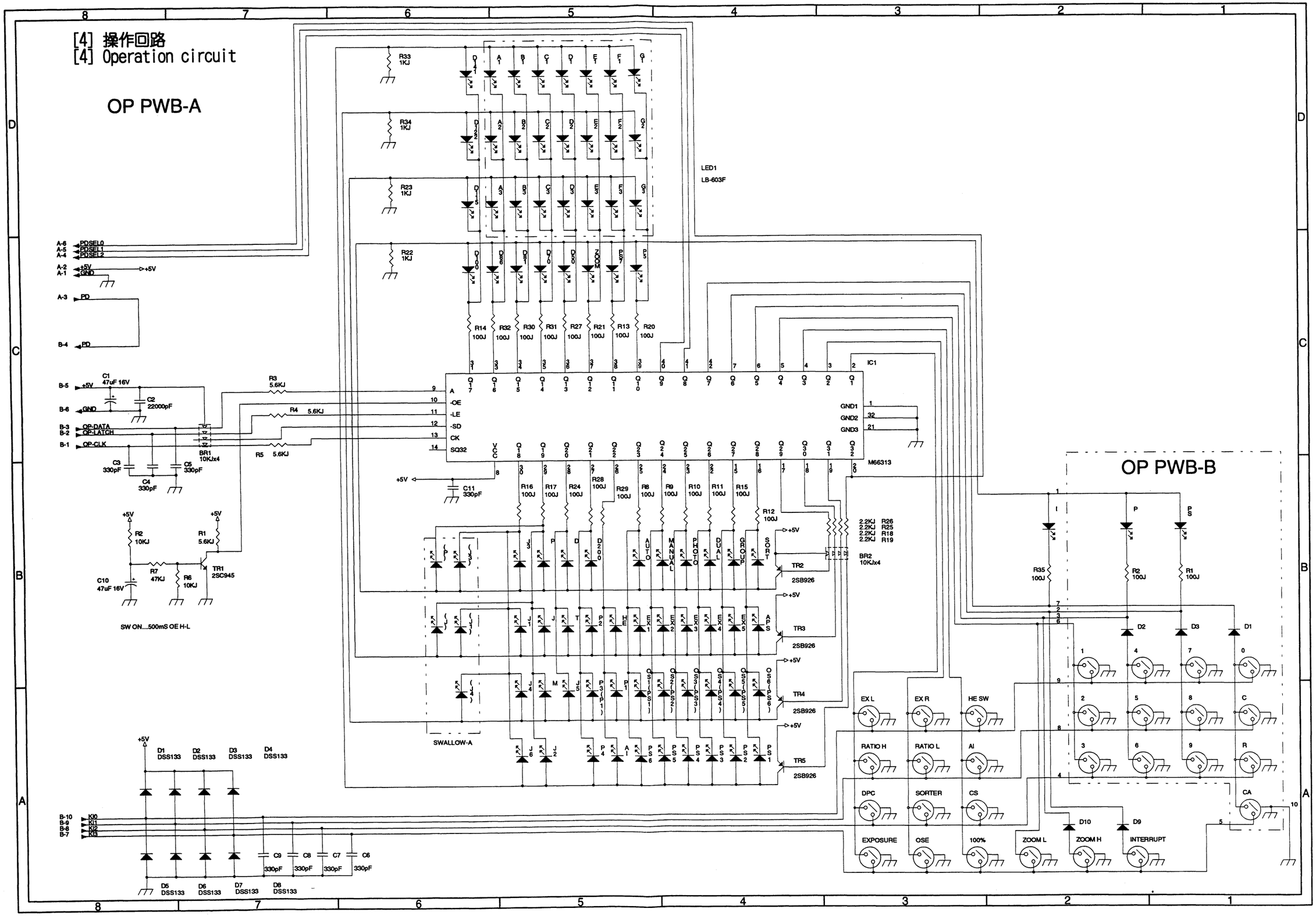
No	SIGNAL
1	SCL
2	SDA
3	SIGNAL GND
4	-RESET





[4] 操作回路
[4] Operation circuit

OP PWB-A

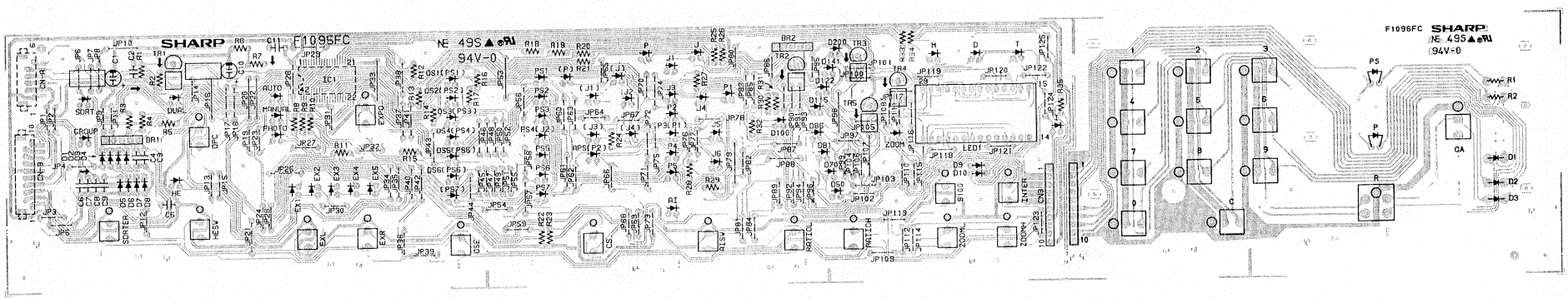


OP PWB-B

操作基板
Operation PWB

CN-A

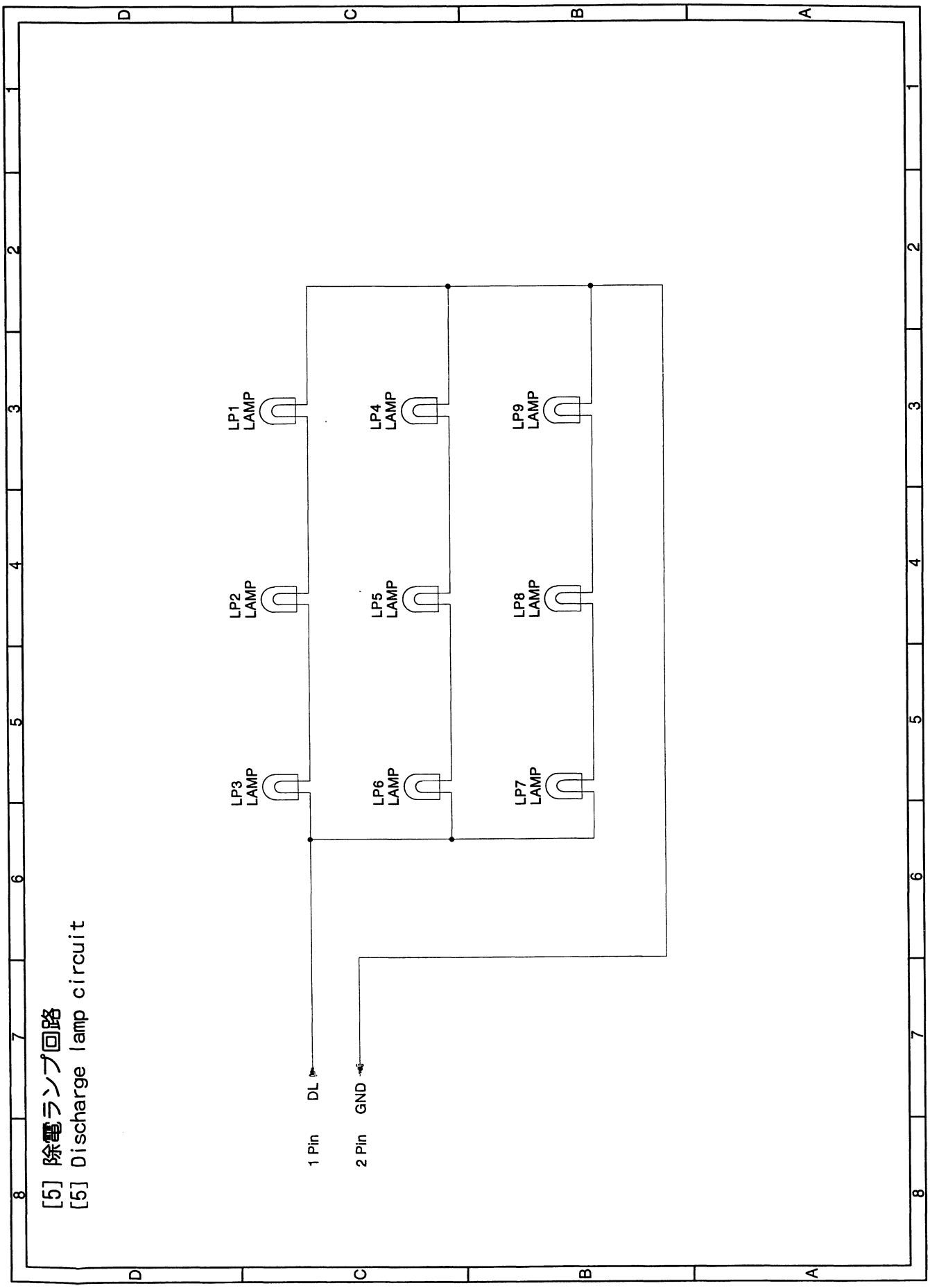
6	PDSEL0
5	PDSEL1
4	PDSEL2
3	PD
2	+5V
1	GND

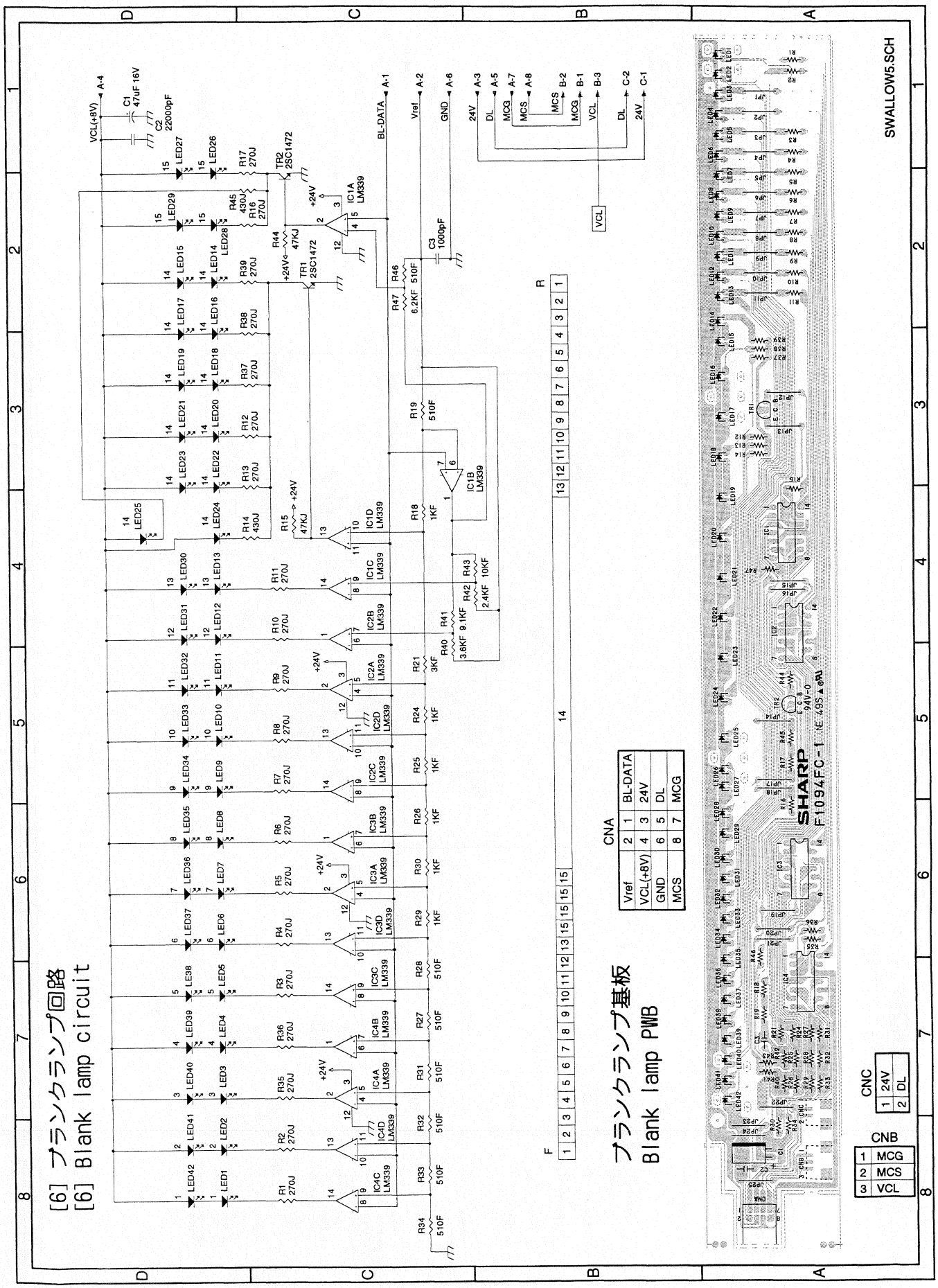


CN-B

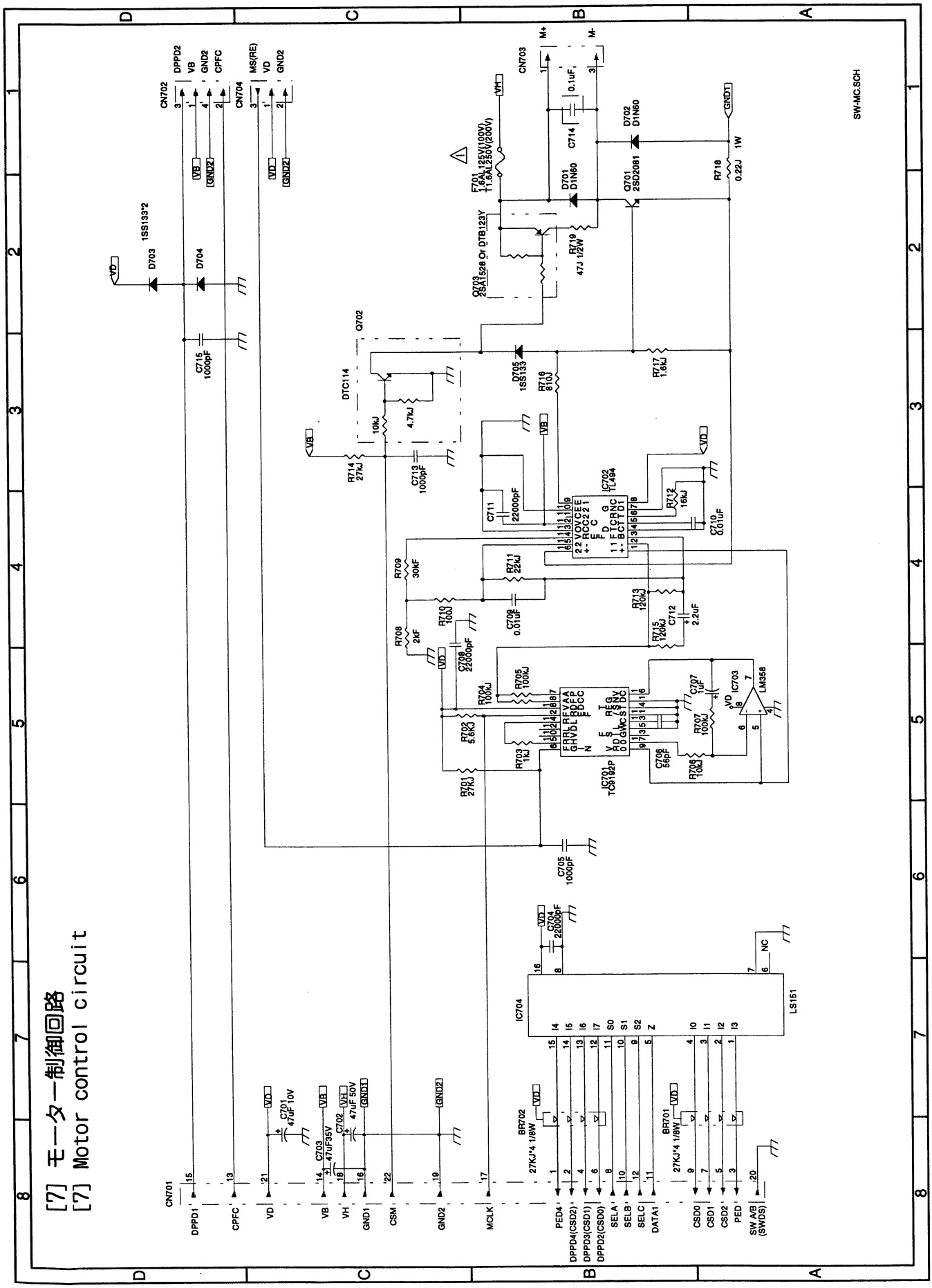
10	K10
9	K11
8	K12
7	K13
6	GND
5	+5V
4	PD
3	OP-DATA
2	OP-LATCH
1	OP-CLK

[5] 除電ランプ回路
[5] Discharge lamp circuit




[6] ブランクランプ回路
[6] Blank lamp circuit
ブランクランプ基板
Blank lamp PWB
SWALLOW5.SCH
SHARP
F1094FC-1
CNA
CNB
CNC
F
R

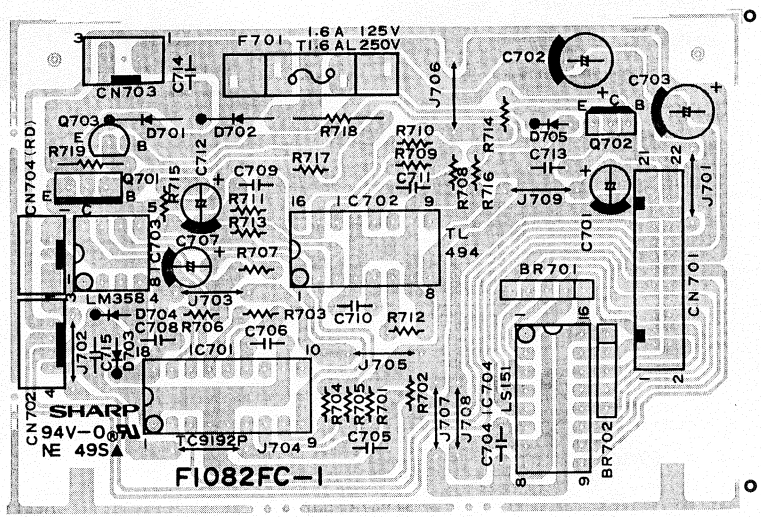
[7] モーター制御回路
[7] Motor control circuit



モーター制御基板
Motor control PWB

CN703

1	M+
2	-----
3	M-



CN704

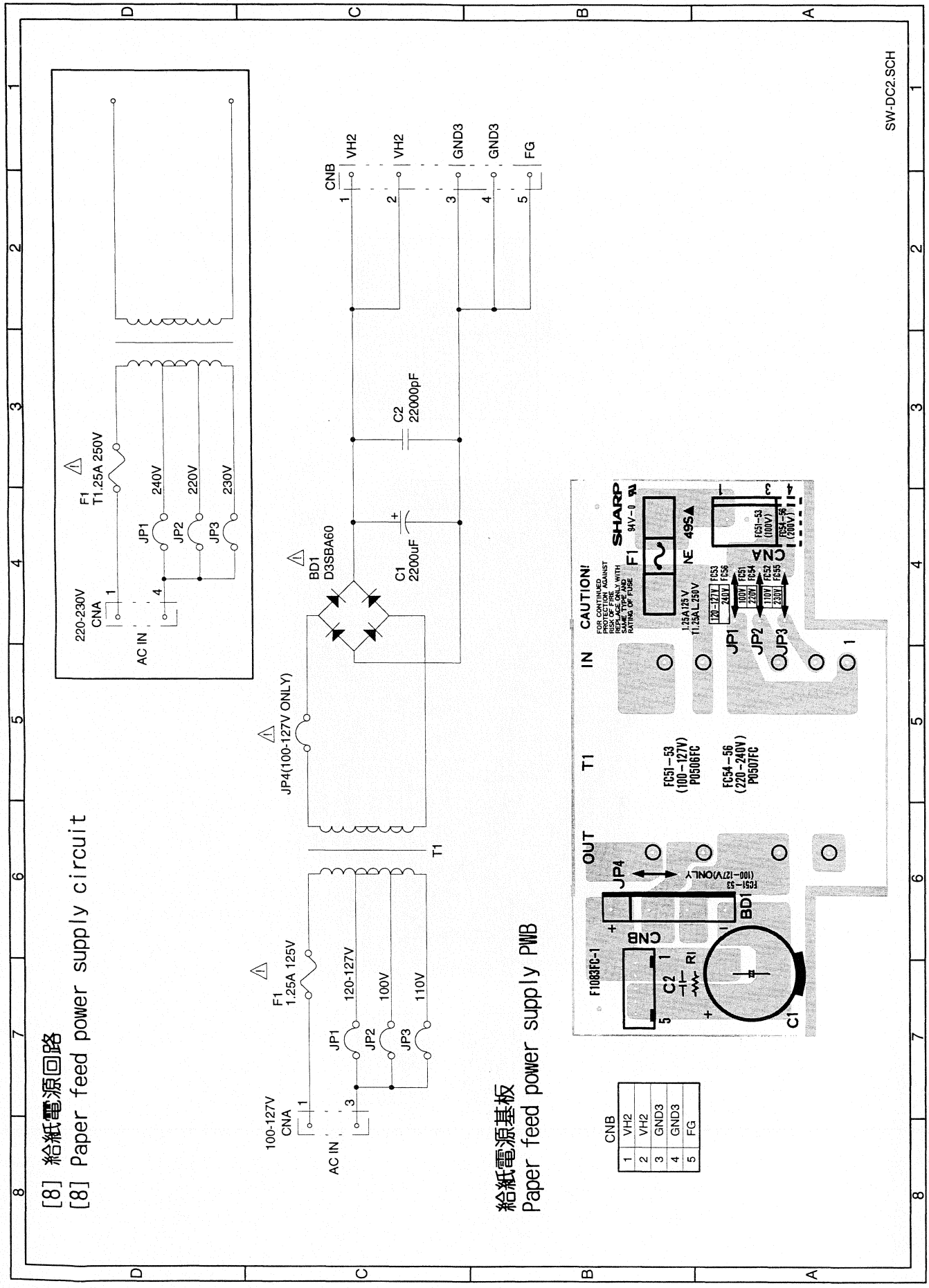
1	VD
2	GND2
3	MS(RE)

CN702

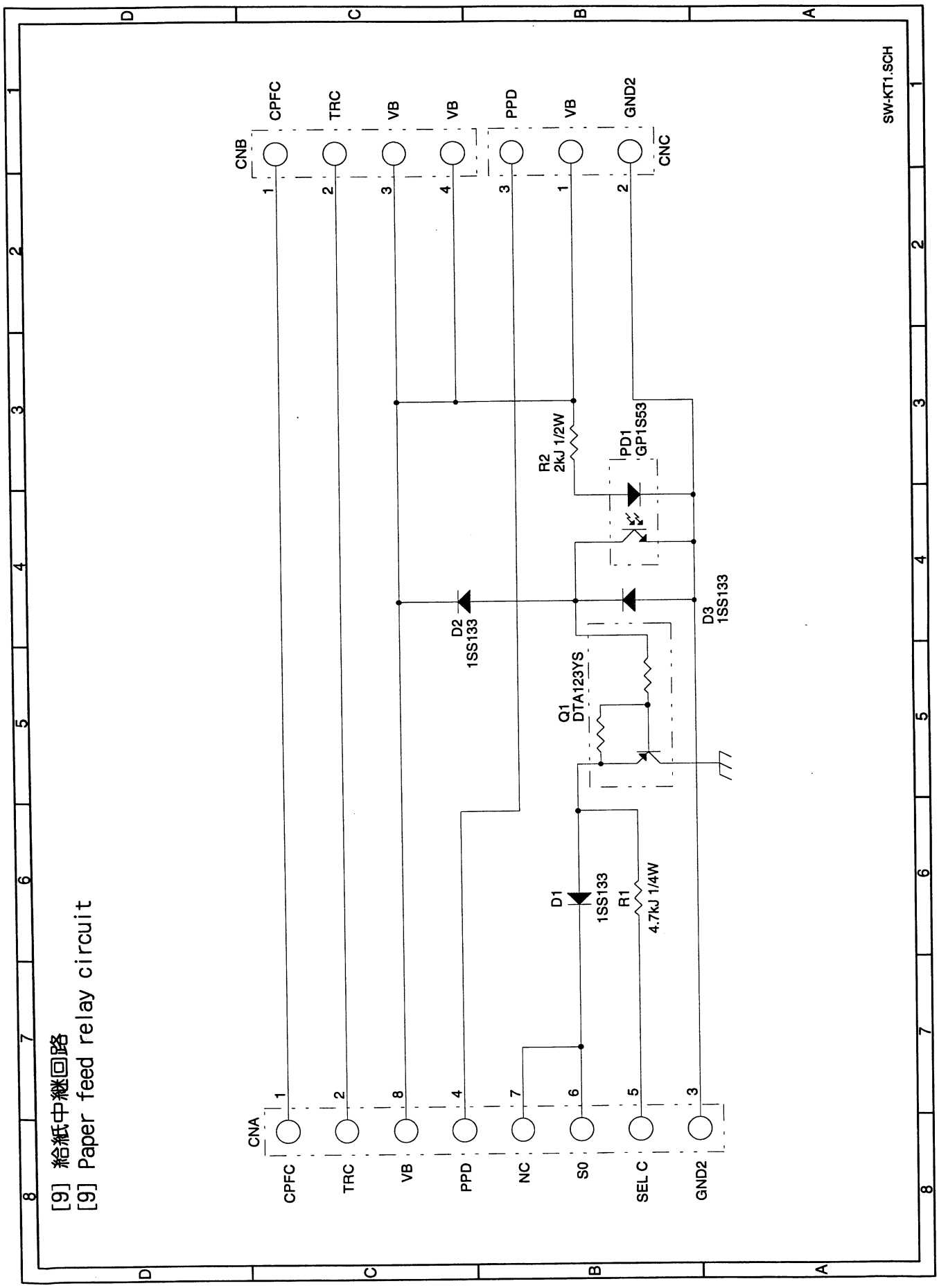
1	VB
2	CPFC
3	DPPD2
4	GND2

CN701

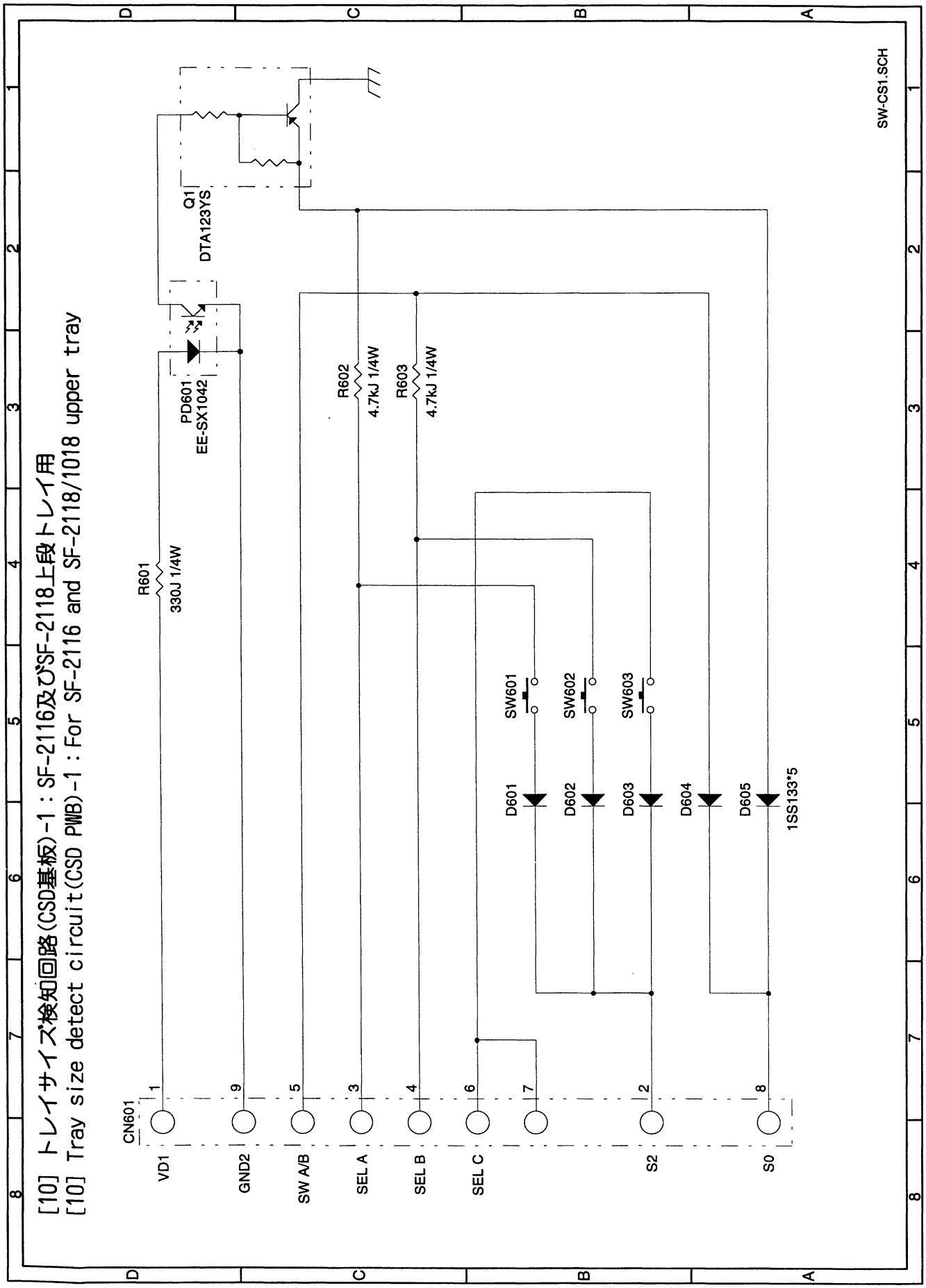
VD	21	22	CSM
GND2	19	20	SW A/B(SWDS)
MCLK	17	18	VH
DPPD1	15	16	GND1
CPFC	13	14	VB
DATA1	11	12	SELC
CSD0	9	10	SELB
CSD1	7	8	SELA
CSD2	5	6	DPPD2 (CSD0)
PED	3	4	DPPD3 (CSD1)
PED4	1	2	DPPD4 (CSD2)


[8] 給紙電源回路
[8] Paper feed power supply circuit
220-230V
CNA
AC IN
F1
T1.25A 250V
JP1
JP2
JP3
240V
220V
230V
100-127V
CNA
AC IN
F1
1.25A 125V
120-127V
100V
110V
JP4(100-127V ONLY)
BD1
D3SBA60
C1
2200uF
C2
22000pF
T1
CNB
VH2
VH2
GND3
GND3
FG
給紙電源基板
Paper feed power supply PWB
CNB
1 VH2
2 VH2
3 GND3
4 GND3
5 FG
F1083FC-1
OUT
IN
T1
CAUTION!
SHARP
F1
1.25A125V
11.25AL250V
NE 49SA
FC51-53
(100-127V)
P0506FC
FC54-56
(220-240V)
P0507FC
JP1
JP2
JP3
JP4
BD1
C1
C2
R1
SW-DC2.SCH

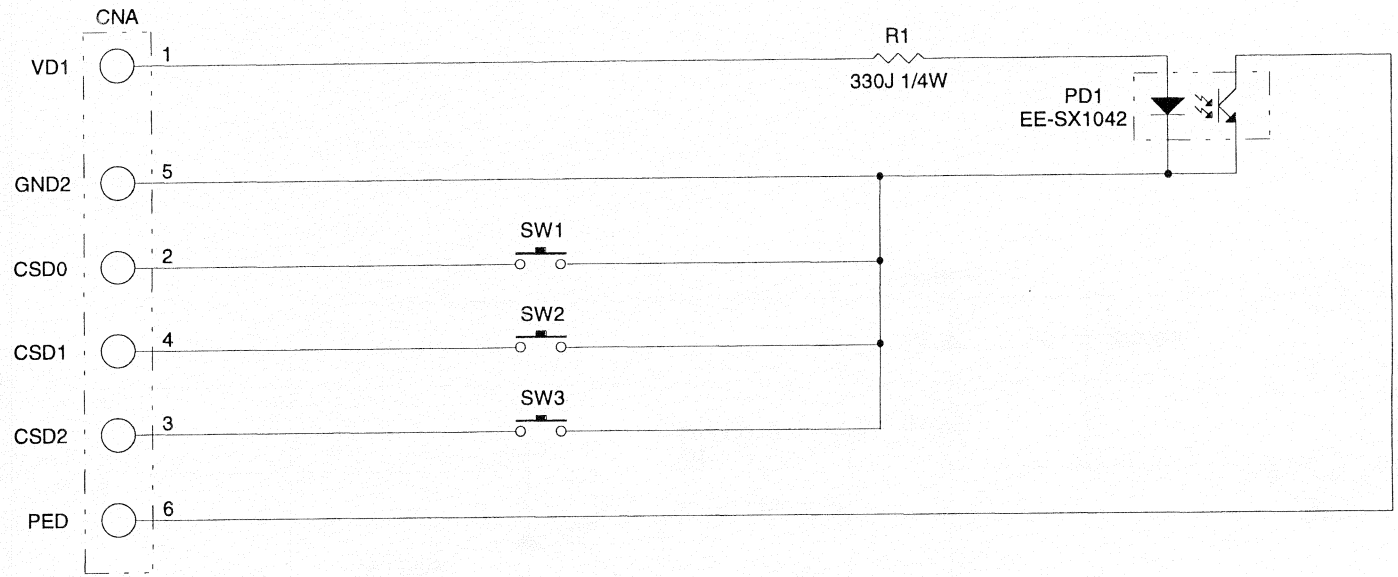
[9] 給紙中継回路
[9] Paper feed relay circuit



[10] トレイサイズ検知回路(CSD基板)-1 : SF-2116及びSF-2118上段トレイ用
[10] Tray size detect circuit(CSD PWB)-1 : For SF-2116 and SF-2118/1018 upper tray



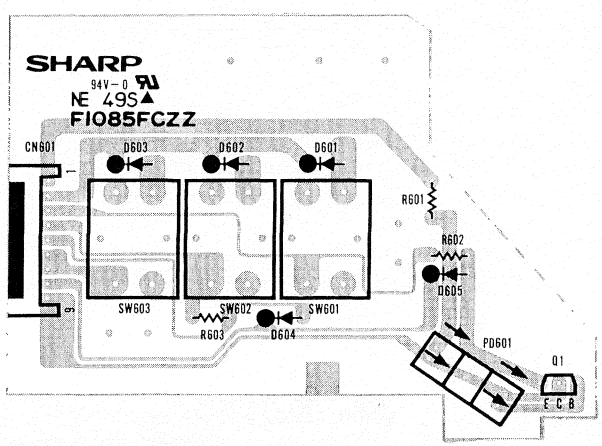
トレイサイズ検知回路(CSD基板)-2：SF-2118/1018及びオプション2段給紙ユニット(SF-CM15)専用
Tray size detect circuit(CSD PWB)-2 : For SF-2118/1018 and option two tray paper feed unit(SF-CM15)



トレイサイズ検知基板ー1
Trad size detect PWB-1

CN601

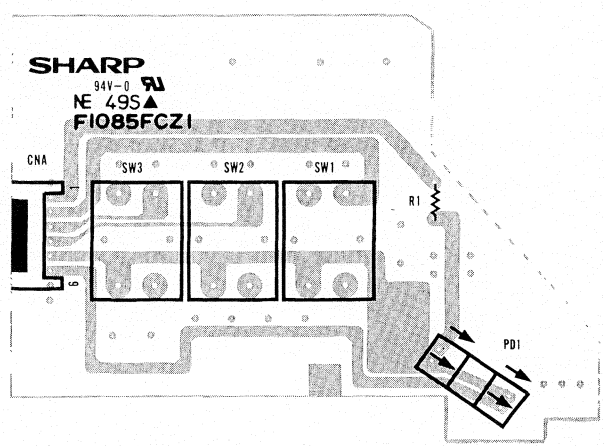
1	VD1
2	S2
3	SELA
4	SELB
5	SW A/B
6	SELC
7	---
8	S0
9	GND2



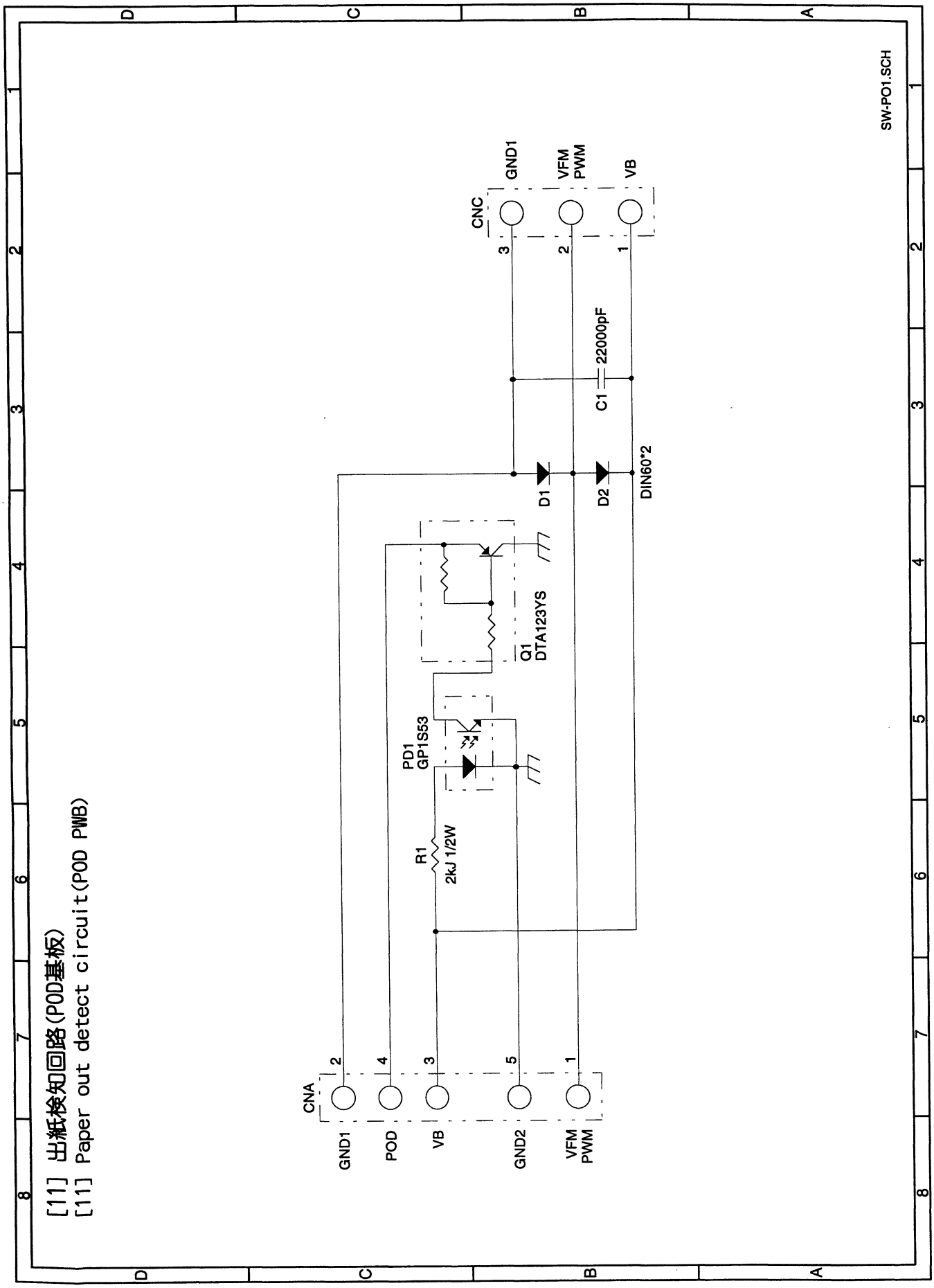
トレイサイズ検知基板ー2
Trad size detect PWB-2

CNA

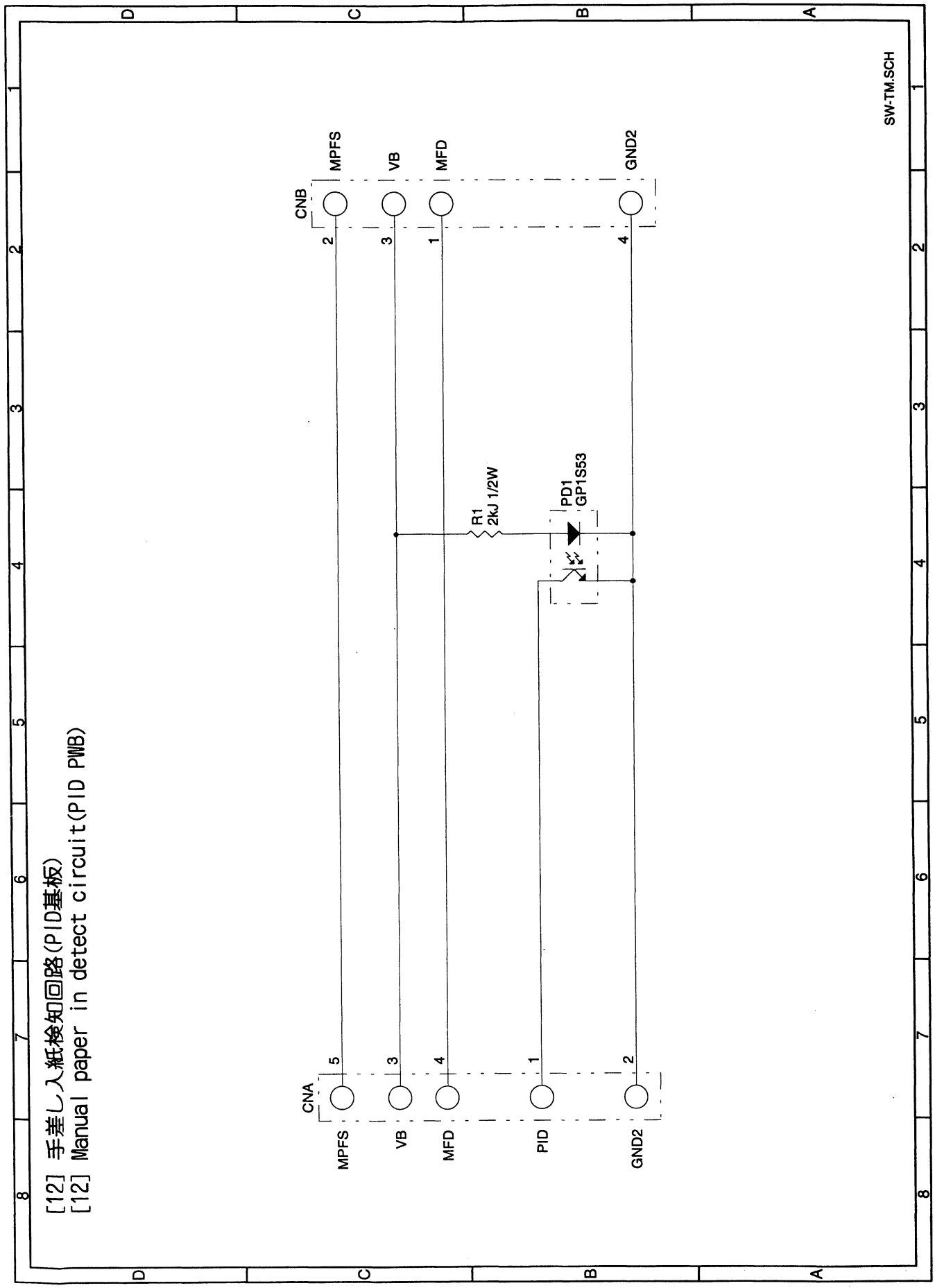
1	VD1
2	CSD0
3	CSD2
4	CSD1
5	GND2
6	PED



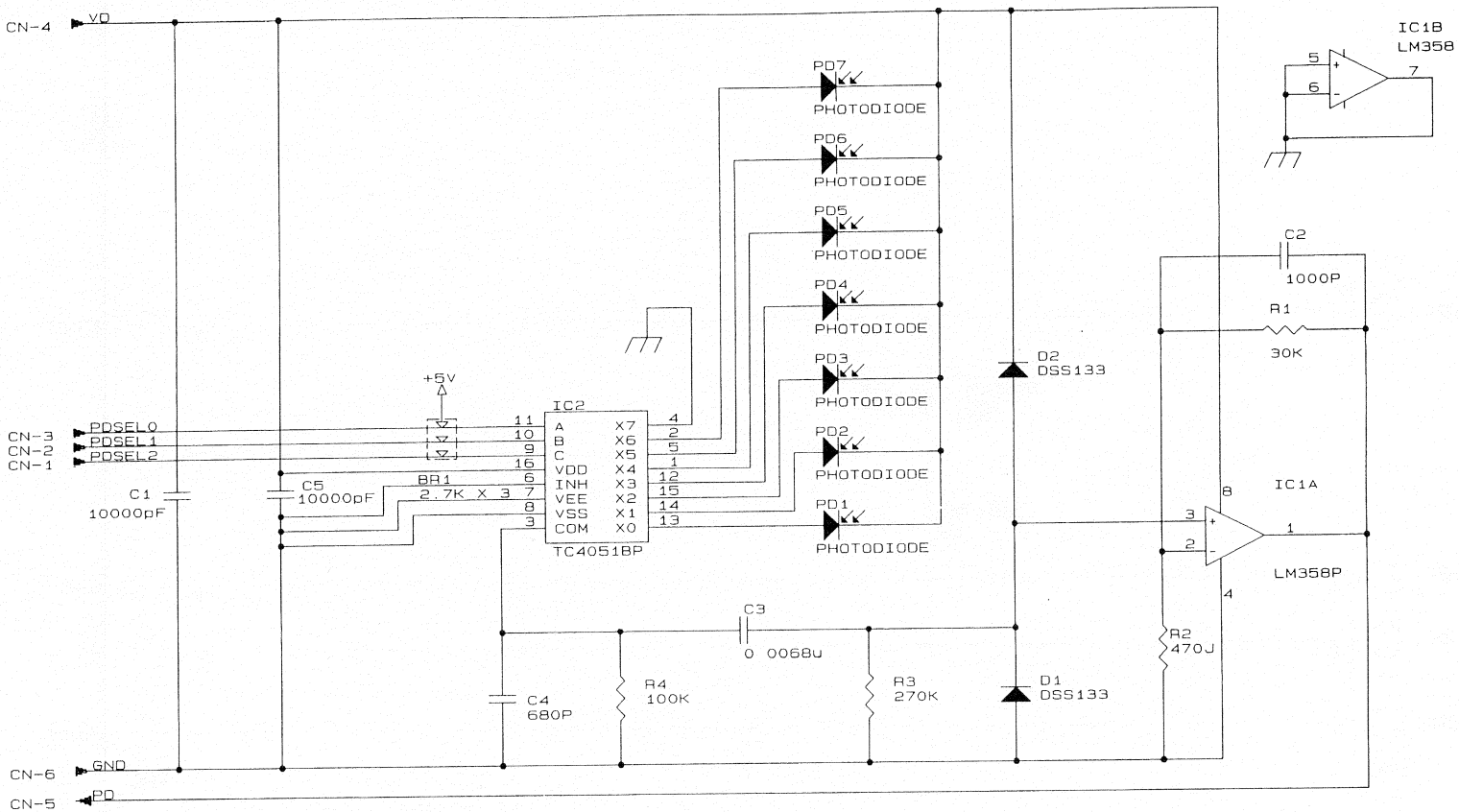
[11] 出紙検知回路(POD基板)
[11] Paper out detect circuit(POD PWB)



[12] 手差し入紙検知回路(PID基板)
[12] Manual paper in detect circuit(PID PWB)



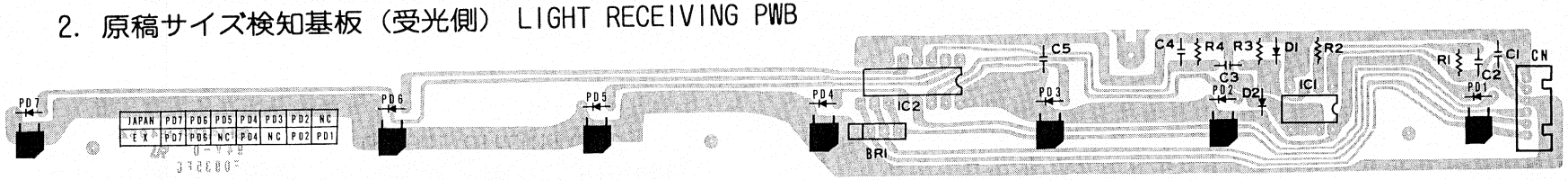
[13] 1. 原稿サイズ検知回路
[13] 1. ORIGINAL SIZE SENSOR CIRCUIT



	PD1	PD2	PD3	PD4	PD5	PD6	PD7
JAPAN CPWBF0935FC31	—	○	○	○	—	○	○
EXCEPT JAPAN CPWBF0935FC32	○	○	—	○	—	○	○

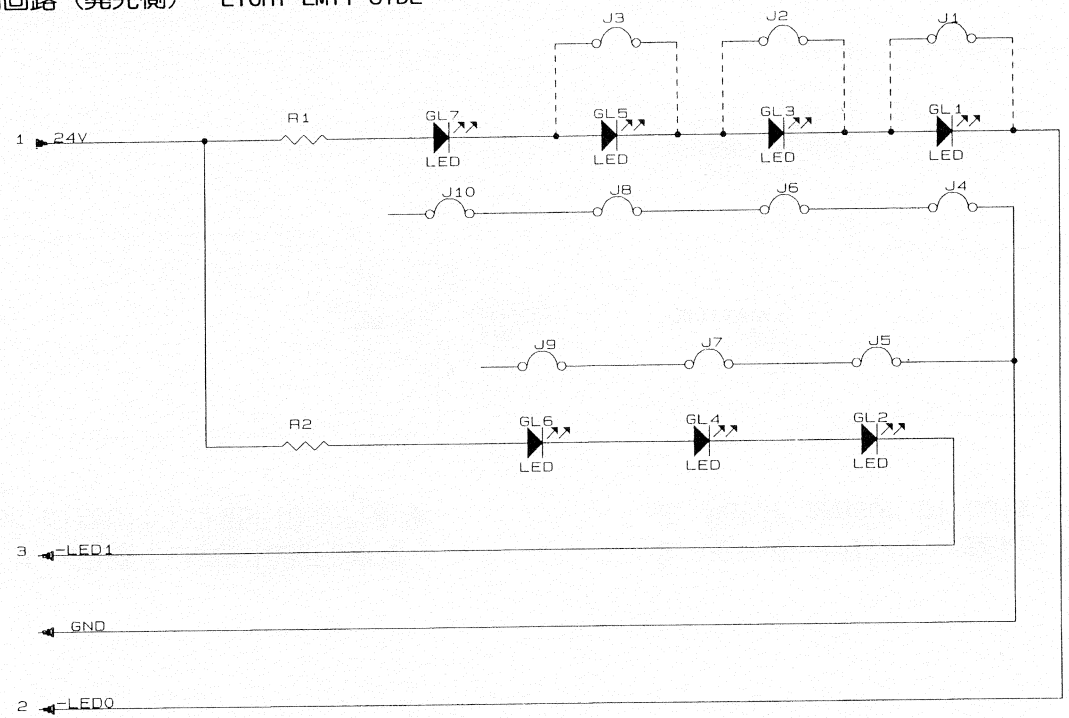
CN	
6	GND
5	PD
4	VD
3	PDSEL0
2	PDSEL1
1	PDSEL2

2. 原稿サイズ検知基板（受光側） LIGHT RECEIVING PWB

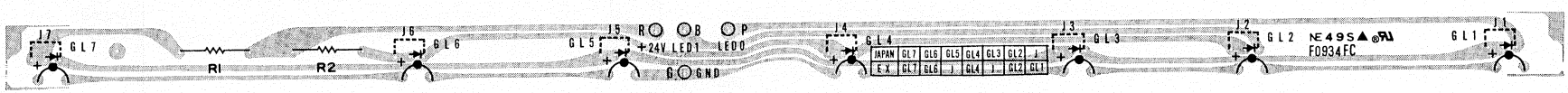


	PD7	PD6	PD5	PD4	PD3	PD2	PD1
JAPAN	PD7	PD6	PD5	PD4	PD3	PD2	NC
EX	PD7	PD6	NC	PD4	NC	PD2	PD1

3. 原稿サイズ検知回路（発光側）　LIGHT EMIT SIDE



4. 原稿サイズ検知基板（発光側）　LIGHT EMIT PWB



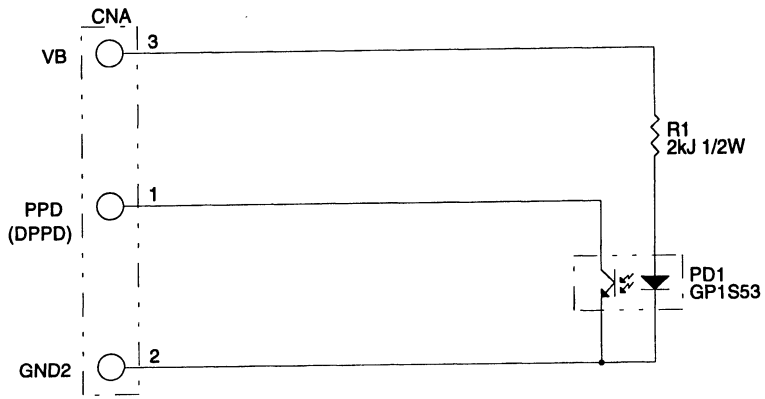
U.CODE	Destination	Fuse resistance (1/2 W) R1	R2	Erement arrangem for distination GL1 PD1	GL2 PD2	GL3 PD3	GL4 PD4	GL5 PD5	GL6 PD6	GL7 PD7	Junper vire J1	J2	J3
CPWBF0934FC31	Japan AB	43J	43J	**	IN (B5)	IN (A4)	IN (B5R)	IN (A4R)	IN (B4)	IN (A3)	IN	**	**
CPWBF0934FC32	Outside Japan Inch	47J	43J	IN (INV)	IN (LI)	**	IN (LIR)	**	IN (LG)	IN (WLT)	**	IN	IN
	Outside Japan AB	47J	43J	IN (A5)	IN (A4)	**	IN (A4R)	**	IN (B4)	IN (A3)	**	IN	IN

Junper vire

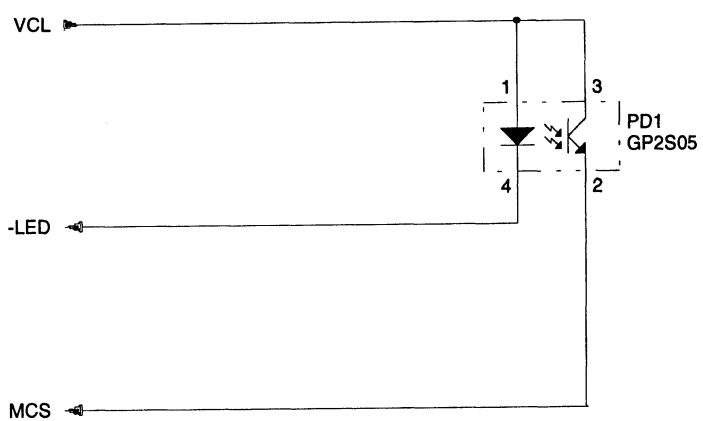
J4, J5, J6, J7, J8, J9, J10

[14] その他の回路
[14] Other circuit

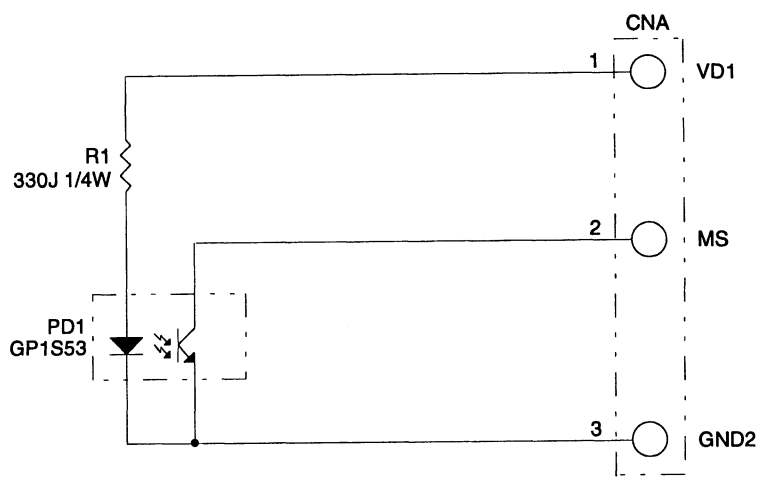
1. 通紙検知回路(PPD/DPPD PWB) : CPWBF1086FC＊＊
1. Paper pass detect circuit(PPD/DPPD PWB) : CPWBF1086FC＊＊



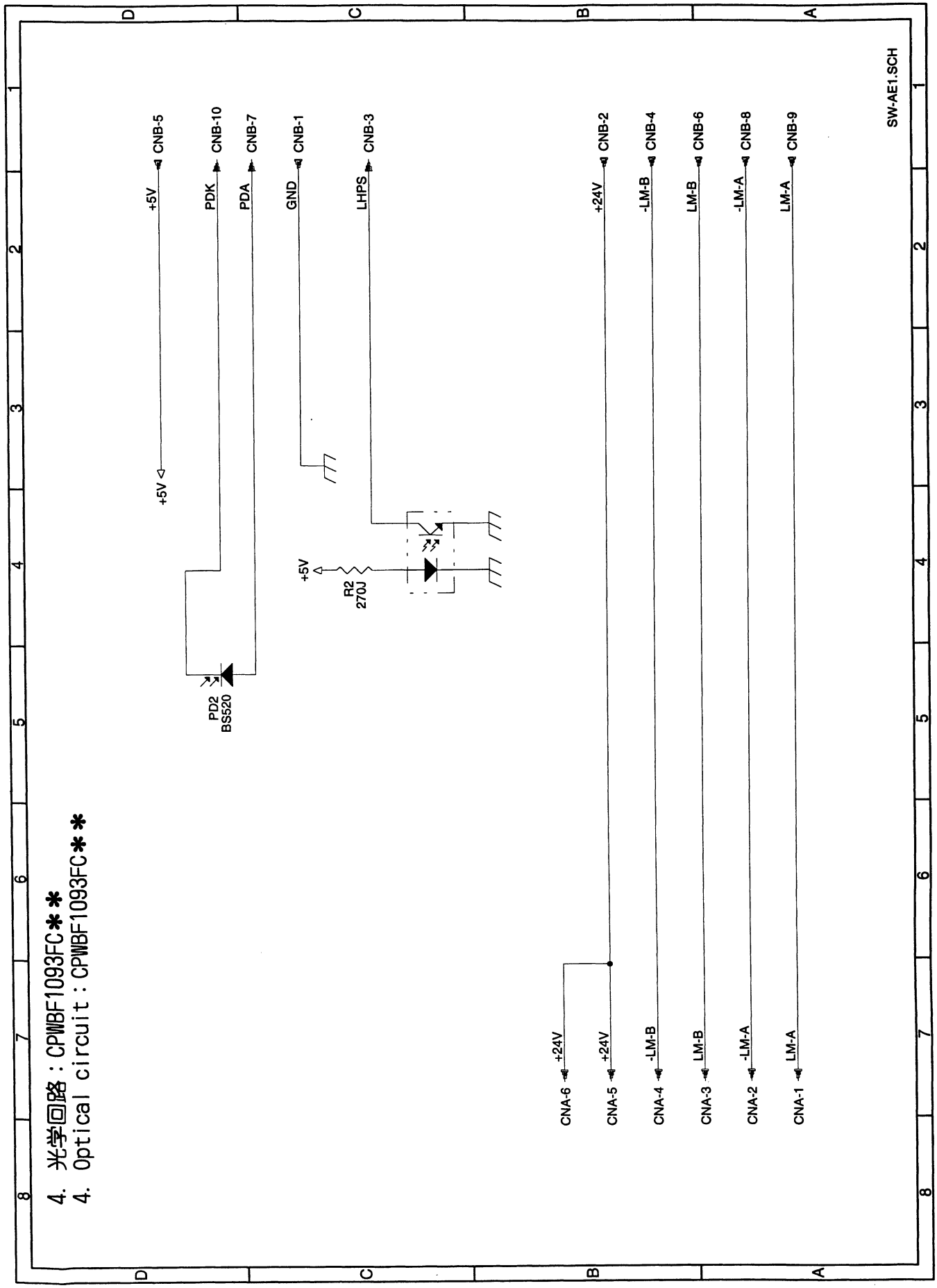
2. マークセンサー回路 : CPWBF1092FC＊＊
2. Mark sensor circuit : CPWBF1092FC＊＊



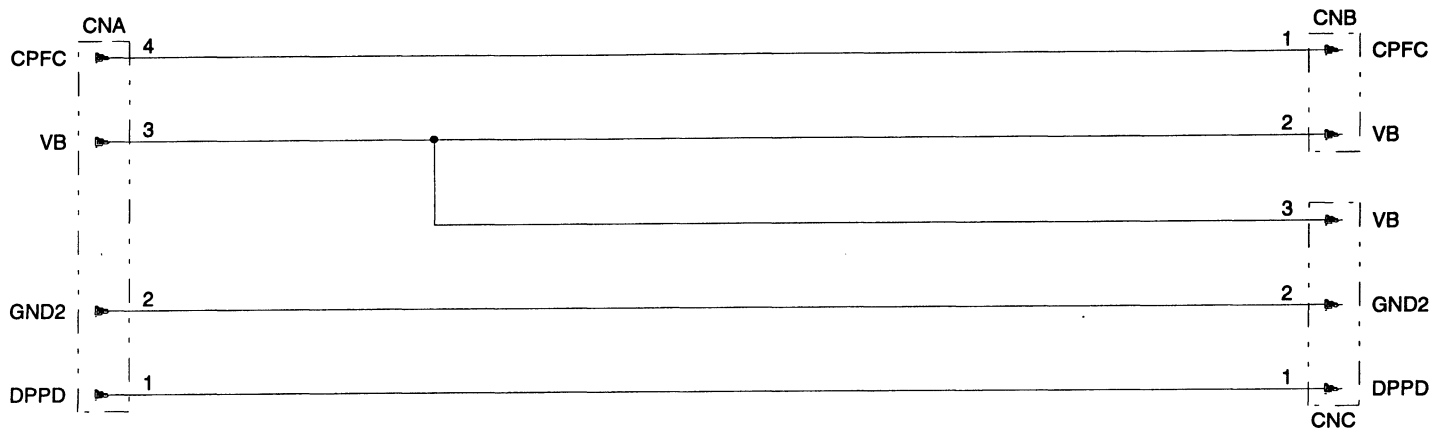
3. モーターセンサー回路 : CPWBF1090FC＊＊
3. Motor sensor circuit : CPWBF1090FC＊＊



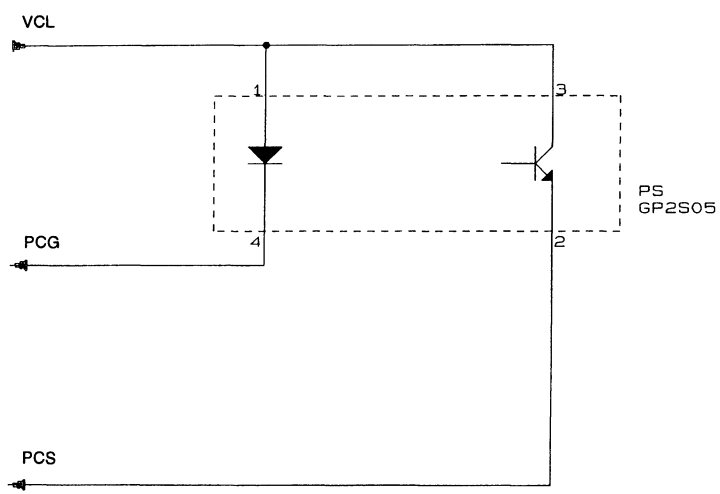
4. 光学回路：CPWBF1093FC**
4. Optical circuit：CPWBF1093FC**



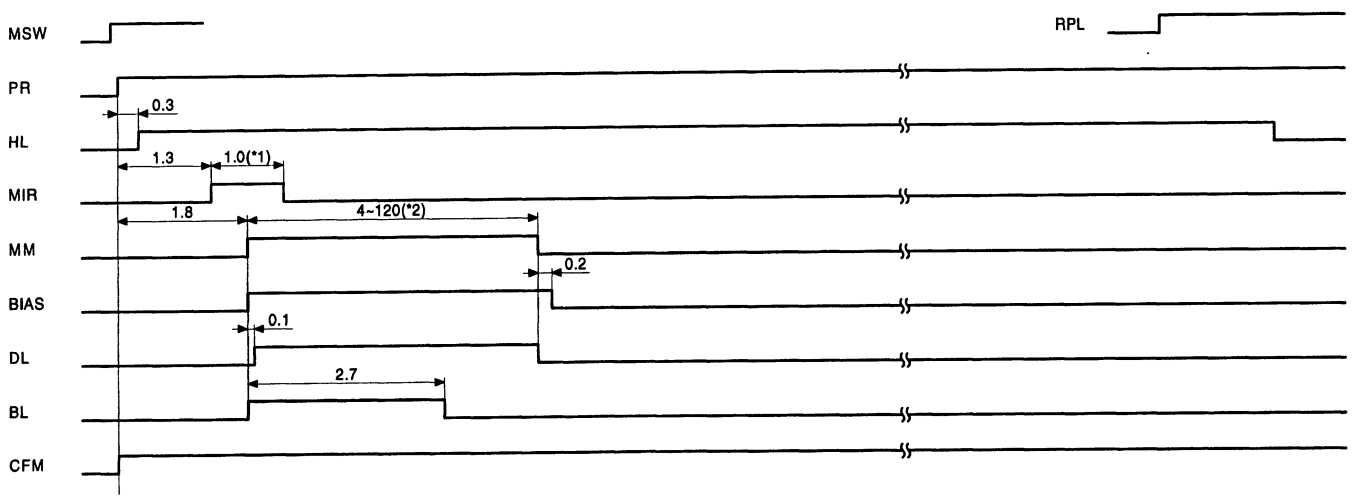
5. カセットモジュール回路：CPWBF1087FC＊＊
5. Cassette module circuit：CPWBF1087FC＊＊



6. プロセスマーキング検知回路
6. Process marking sensor circuit



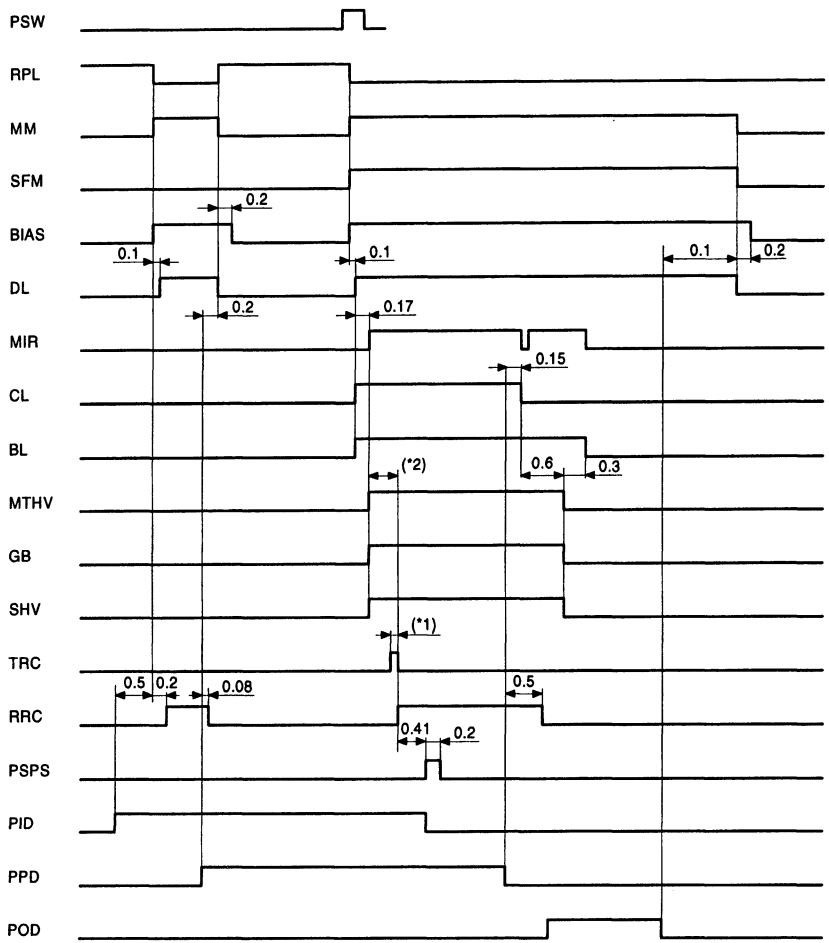
[15] タイミングチャート
[15] Timing chart
1. ウォームアップ
1. Warm up



*1 ミラー停止位置により異なる
*2 マークセンサー検知、トナー濃度検知状態により異なる

*1 Differs depending on the mirror stop position
*2 Differs depending on the mark sensor detection and the toner density detection state

2. SF-2118/2116/1018/1016 シングル手差し A4給紙
2. SF-2118/2116/1018/1016 single manual feed A4

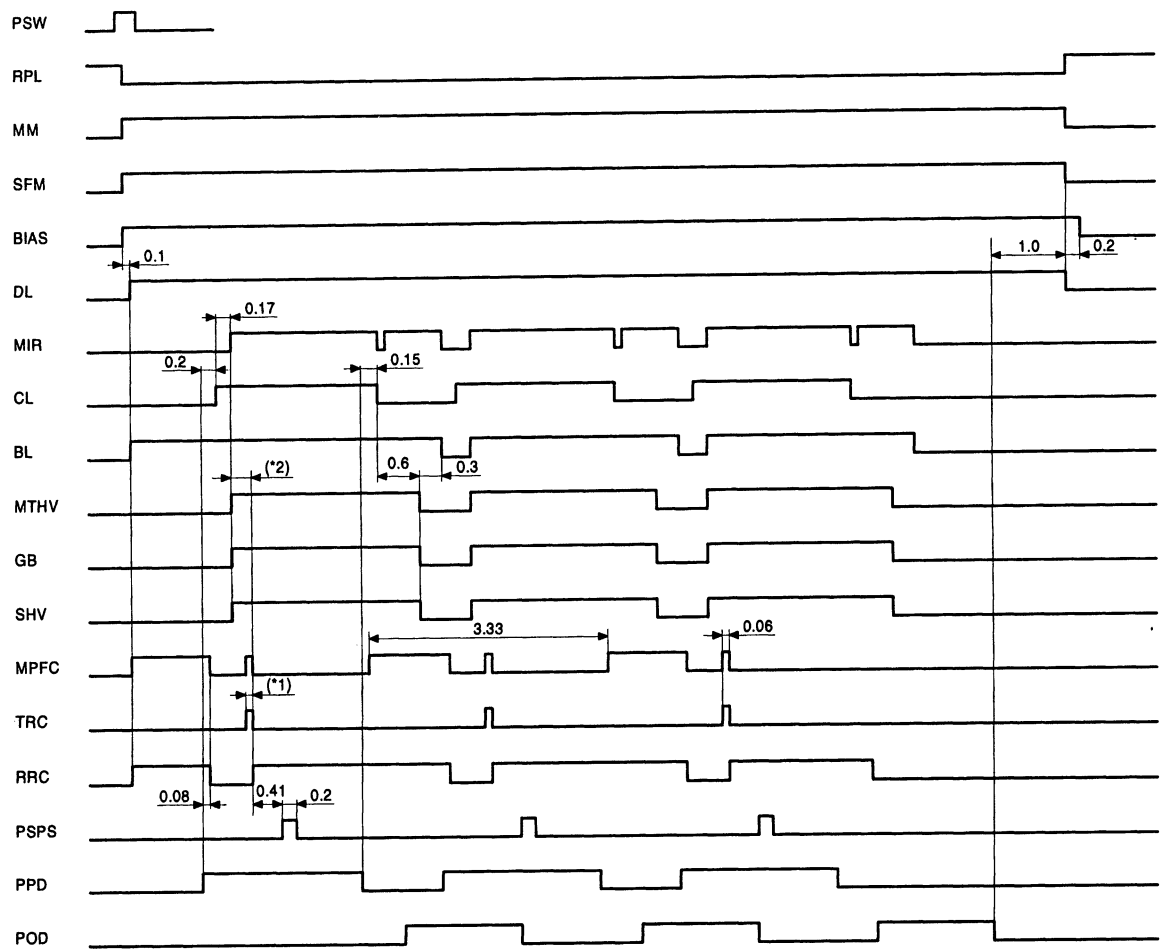


*1 レジスト量（シミュレーション51-2で設定）
*2 用紙先端位置（シミュレーション50-1で設定）

*1 Resist amount (Set with simukation 51-2.)
*2 Paper lead edge position (Set with simulation 50-1.)

3. SF-2118/1018 マルチ手差し A4給紙

3. SF-2118/1018 multi manual feed A4

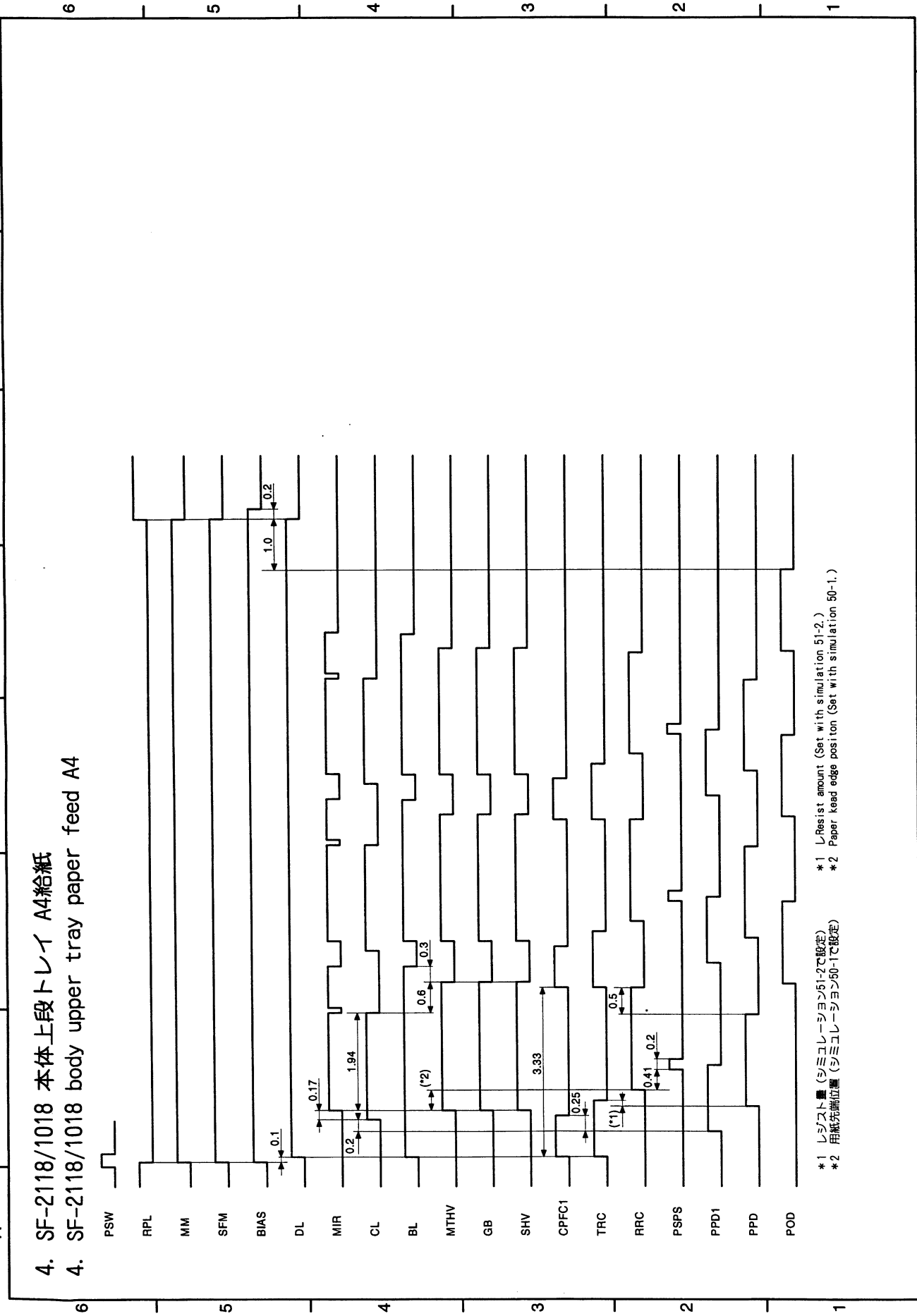


*1 レジスト量（シミュレーション51-2で設定）
*2 用紙先端位置（シミュレーション50-1で設定）

*1 Resist amount (Set with simulation 51-2.)
*2 Paper lead edge position (Set with simulation 50-1.)

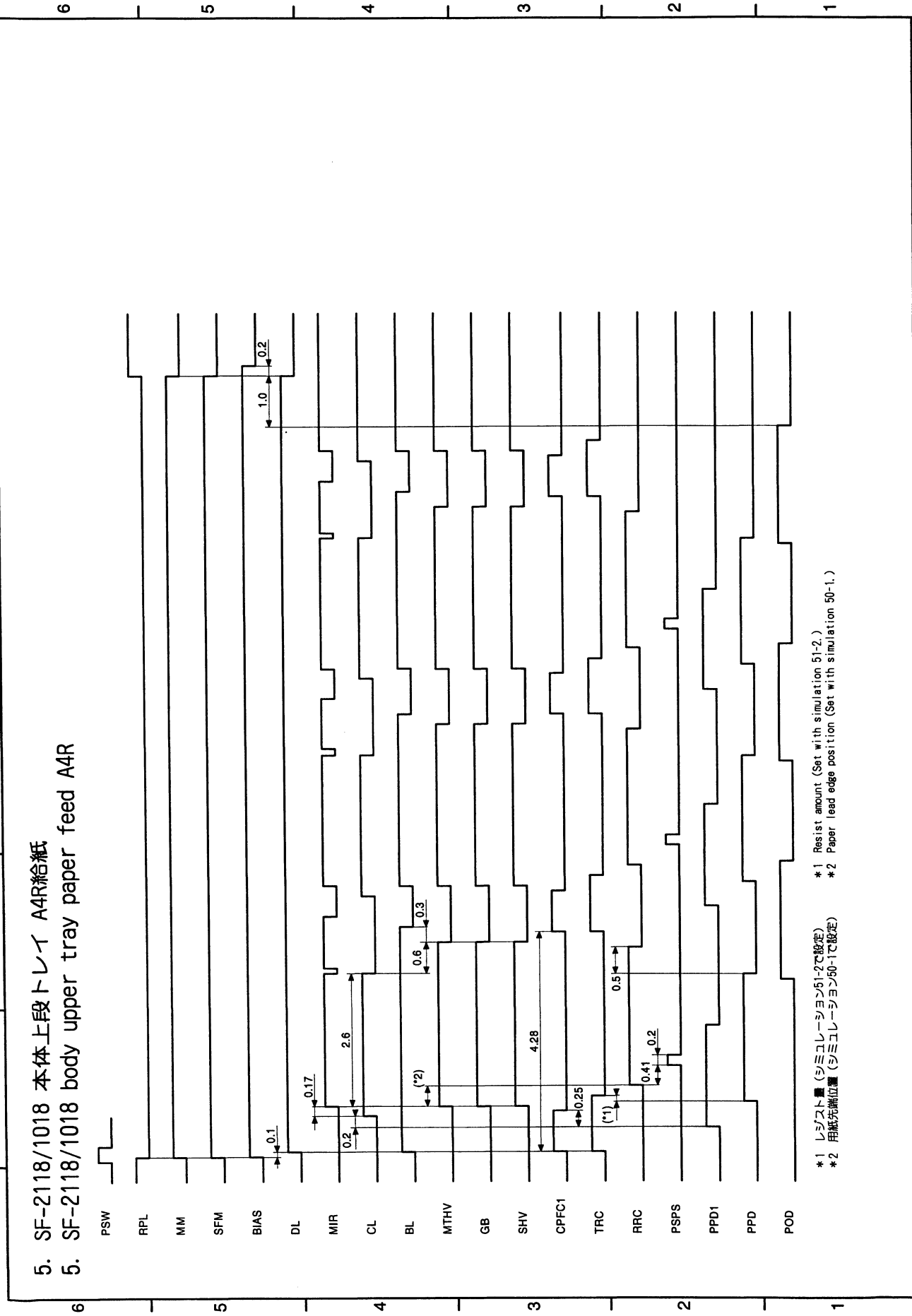
4. SF-2118/1018 本体上段トレイ A4給紙

4. SF-2118/1018 body upper tray paper feed A4



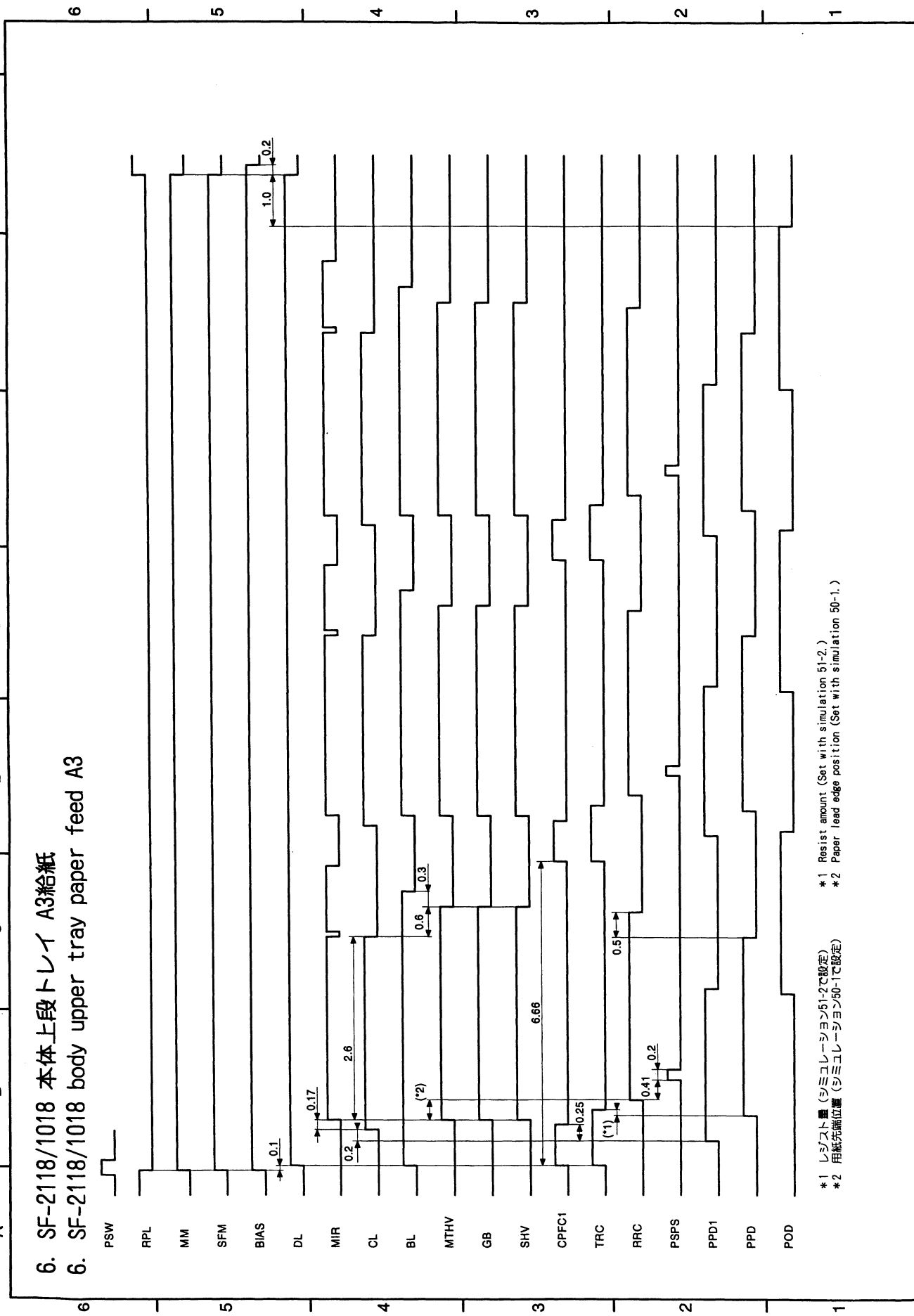
5. SF-2118/1018 本体上段トレイ A4R給紙

5. SF-2118/1018 body upper tray paper feed A4R

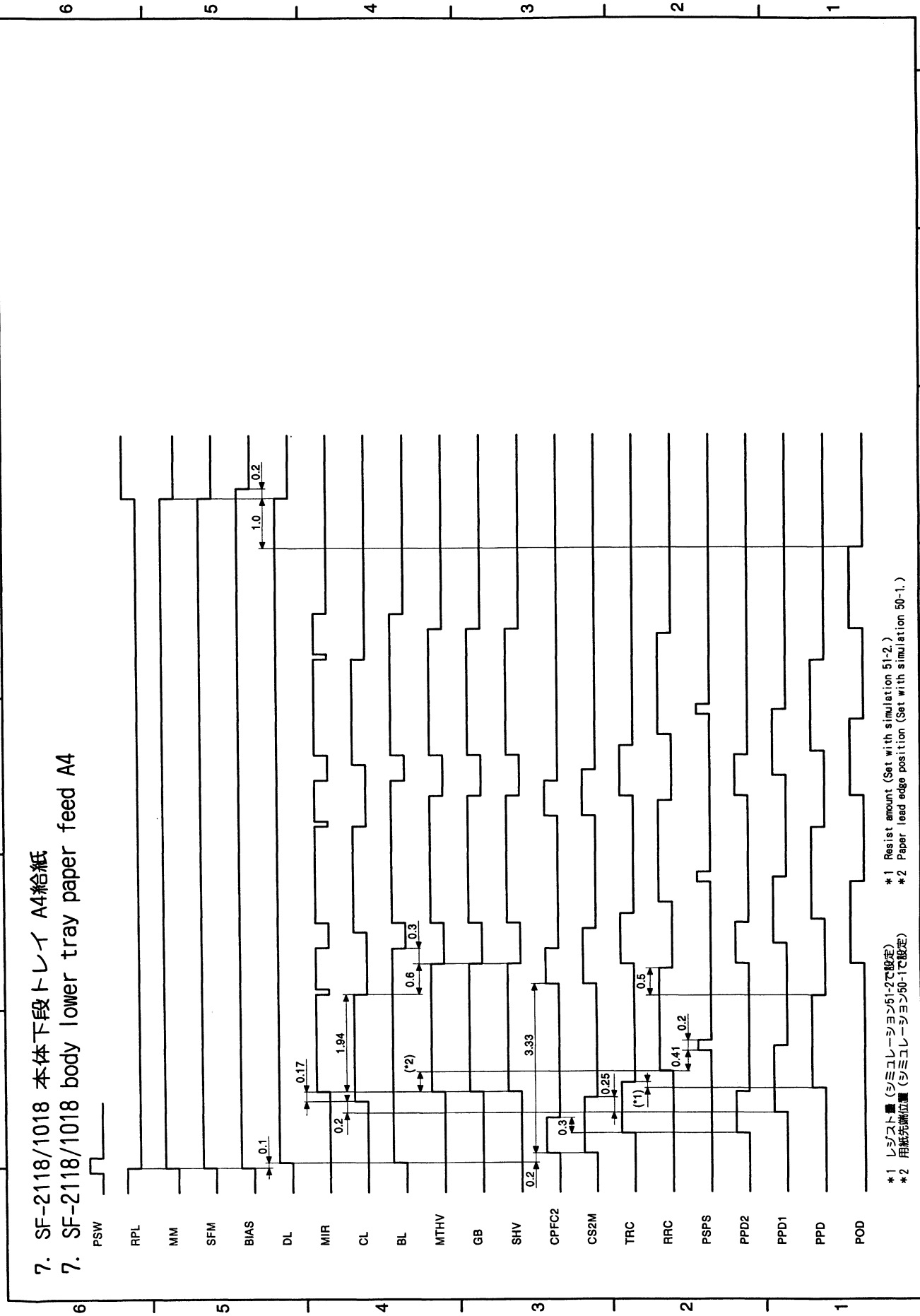


6. SF-2118/1018 本体上段トレイ A3給紙

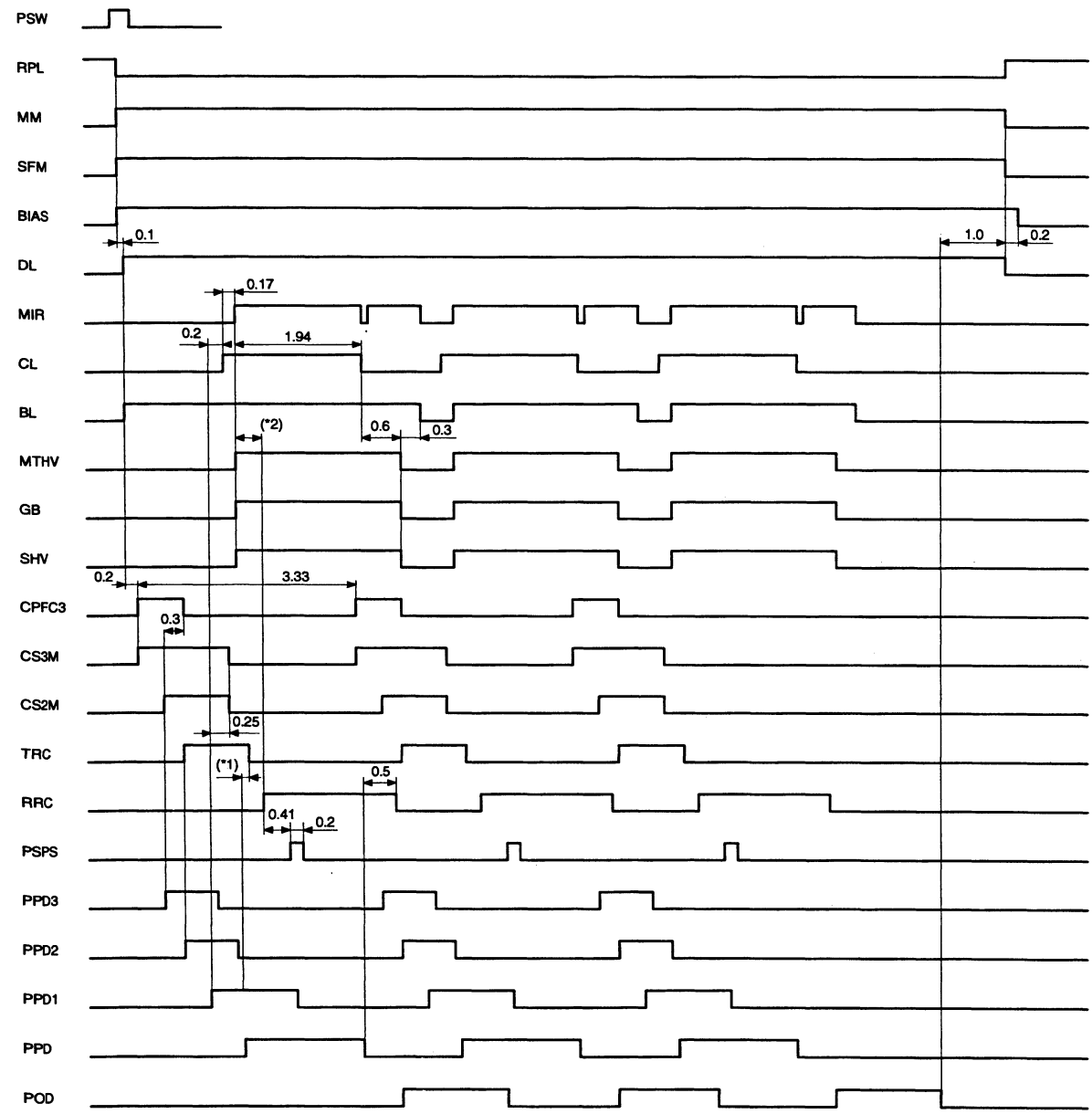
6. SF-2118/1018 body upper tray paper feed A3



7. SF-2118/1018 本体下段トレイ A4給紙
7. SF-2118/1018 body lower tray paper feed A4



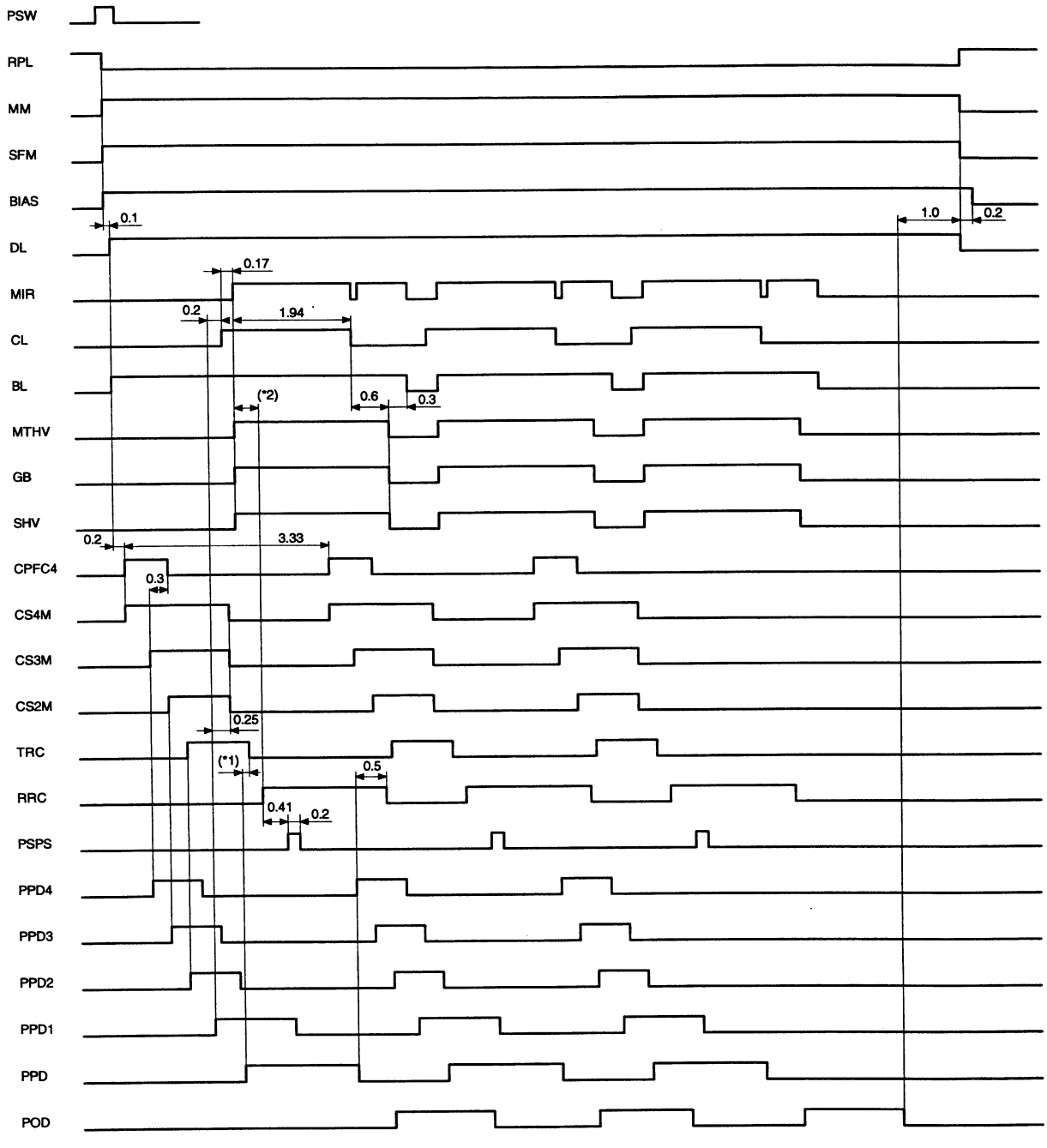
8. SF-2118/1018 オプション給紙上段トレイ A4給紙
8. SF-2118/1018 Option paper feed upper tray paper feed A4



*1 レジスト量（シミュレーション51-2で設定）
*2 用紙先端位置（シミュレーション50-1で設定）

*1 Resist amount (Set with simulation 51-2.)
*2 Paper lead edge position (Set with simulation 50-1.)

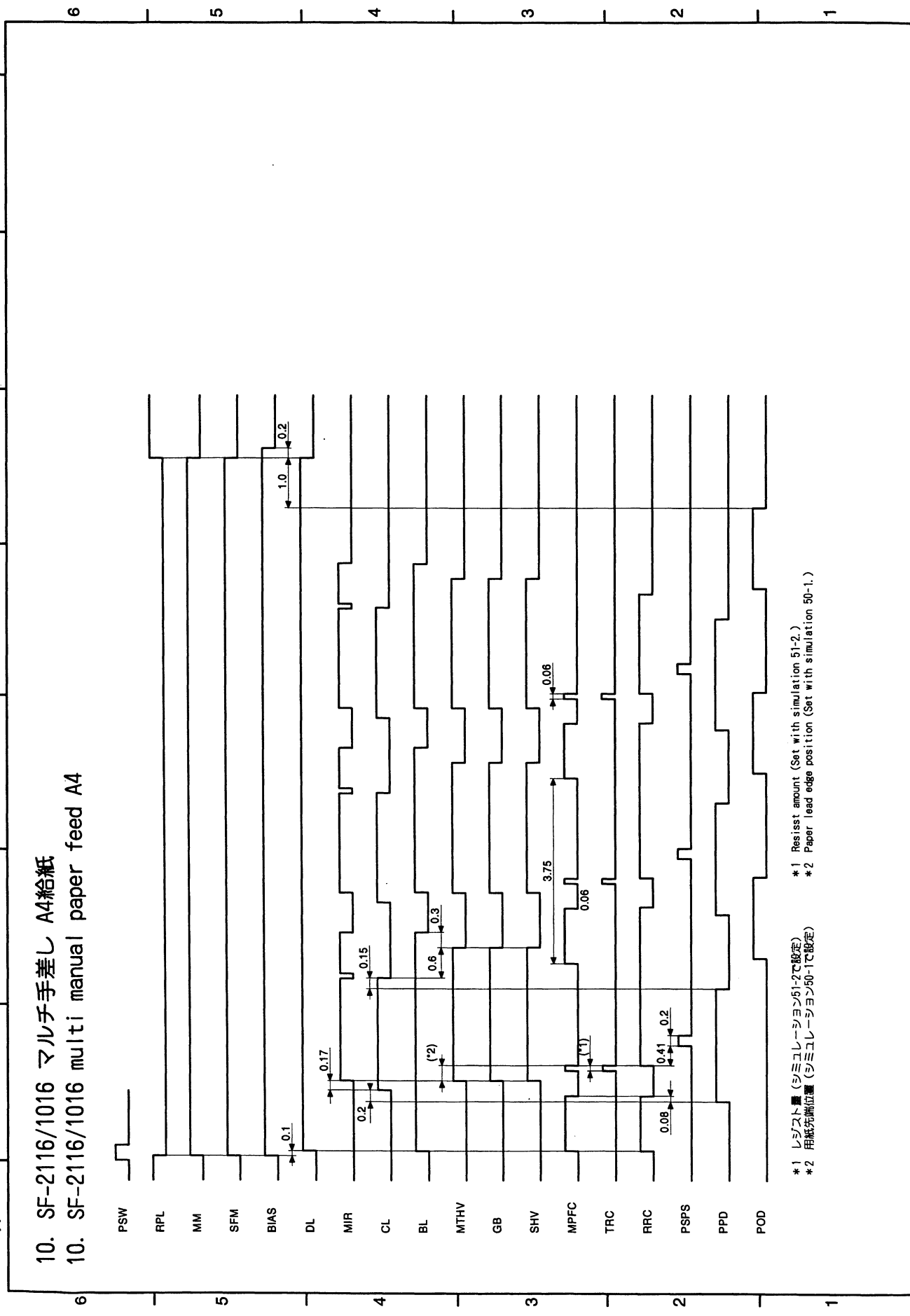
9. SF-2118/1018 オプション給紙下段トレイ A4給紙
9. SF-2118/1018 Option paper feed lower tray paper feed A4



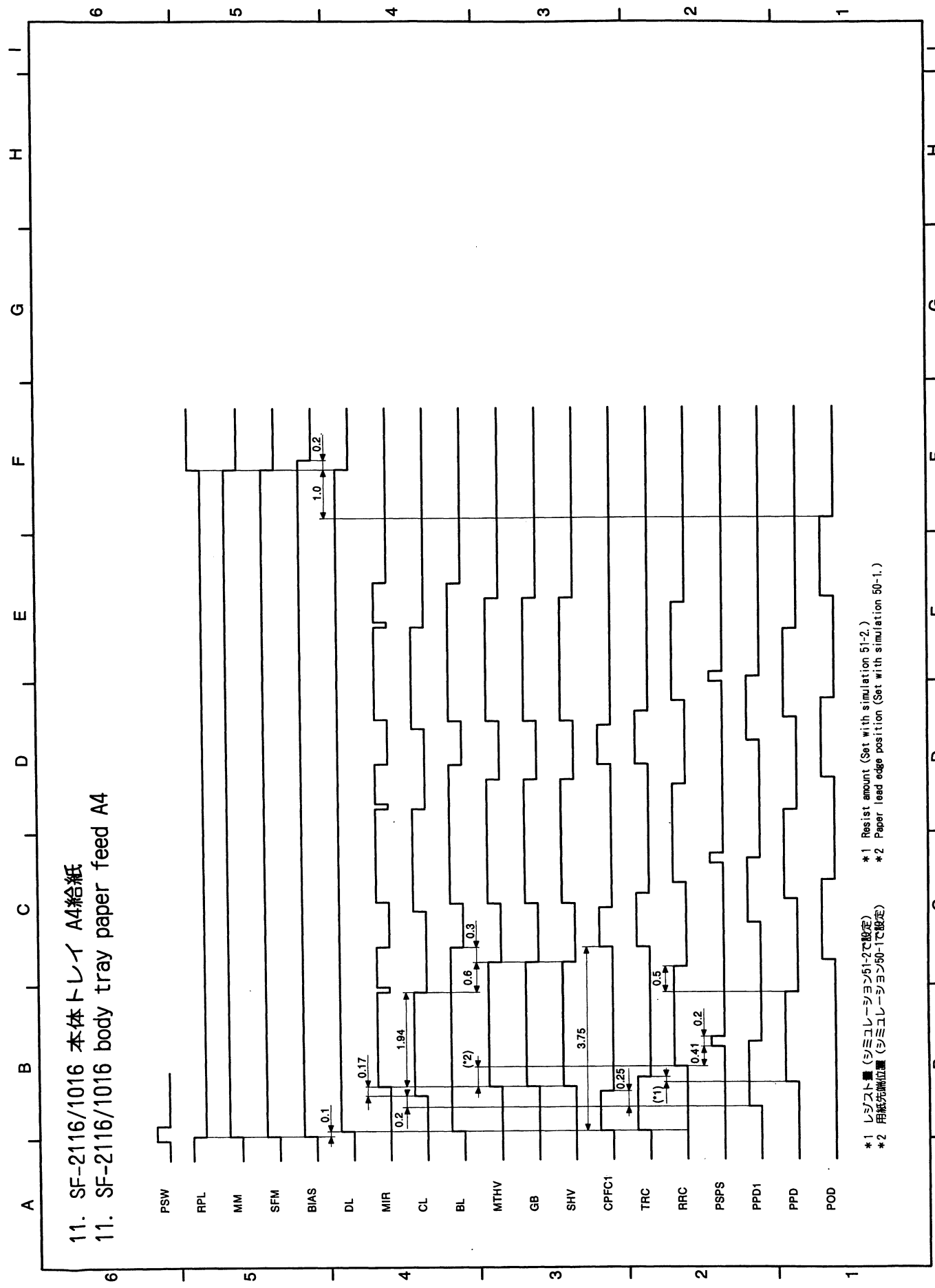
＊1 レジスト量（シミュレーション51-2で設定）
＊2 用紙先端位置（シミュレーション50-1で設定）

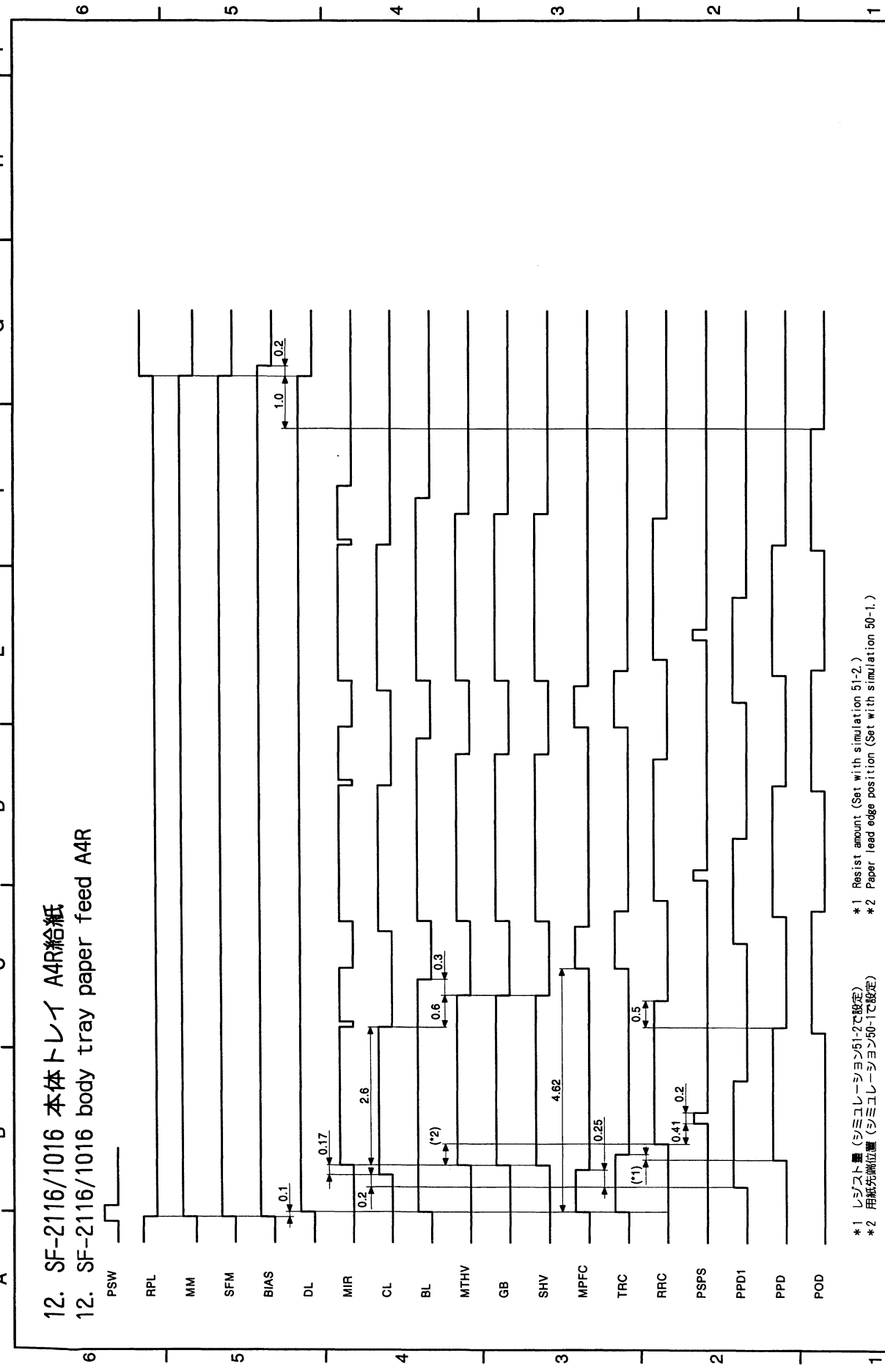
＊1 Resist amount (Set with simulation 51-2.)
＊2 Paper lead edge position (Set with simulation 50-1.)

10. SF-2116/1016 マルチ手差し A4給紙
10. SF-2116/1016 multi manual paper feed A4

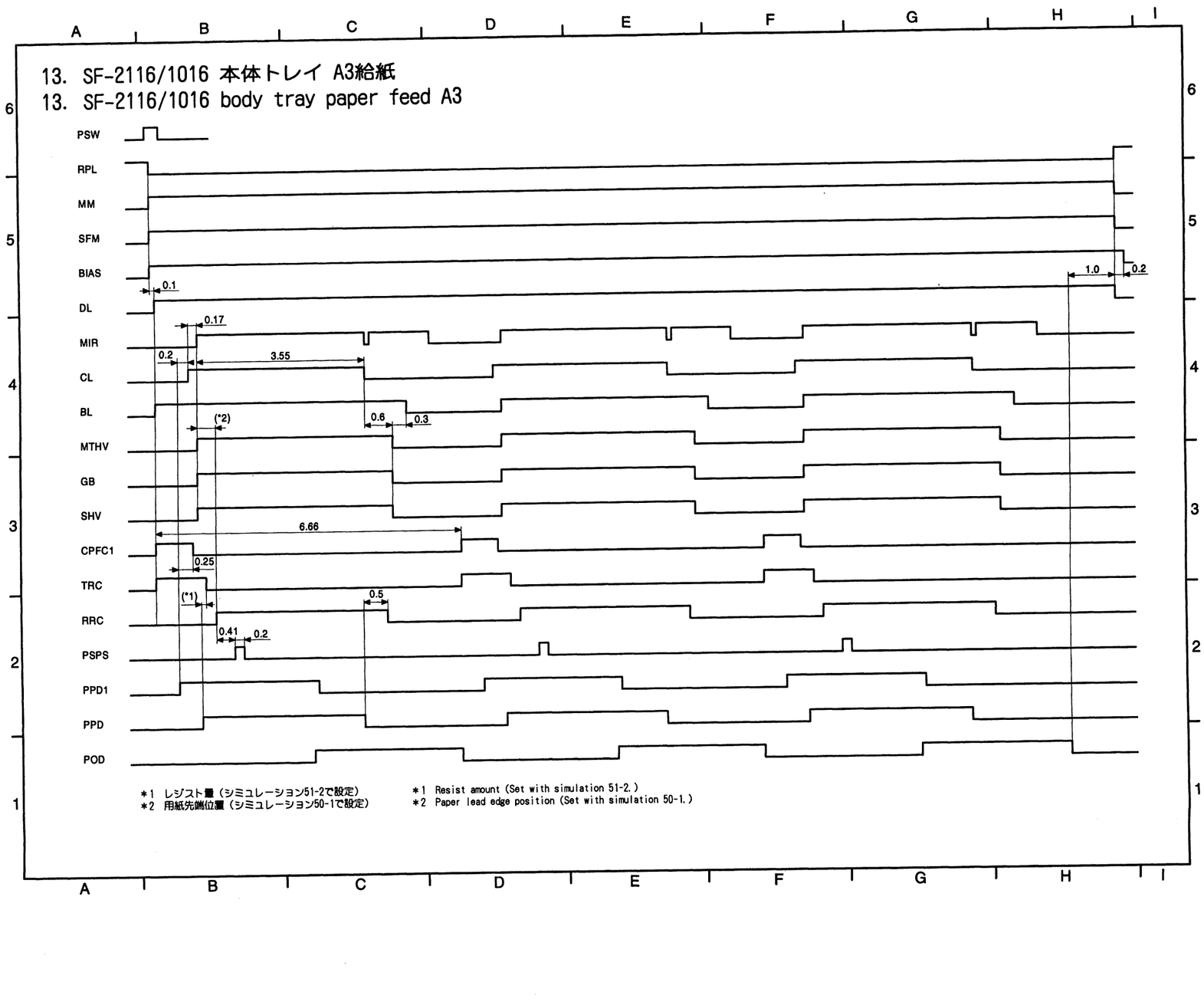


11. SF-2116/1016 本体トレイ A4給紙
11. SF-2116/1016 body tray paper feed A4




12. SF-2116/1016 本体トレイ A4R給紙
12. SF-2116/1016 body tray paper feed A4R
PSW
RPL
MM
SFM
BIAS
DL
MIR
CL
BL
MTHV
GB
SHV
MPFC
TRC
RRC
PSPS
PPD1
PPD
POD
0.1
0.2
1.0
0.2
0.17
2.6
0.2
(*2)
0.3
0.6
4.62
0.25
(*1)
0.5
0.41
0.2
*1 レジスト量（シミュレーション51-2で設定）
*2 用紙先端位置（シミュレーション50-1で設定）
*1 Resist amount (Set with simulation 51-2.)
*2 Paper lead edge position (Set with simulation 50-1.)

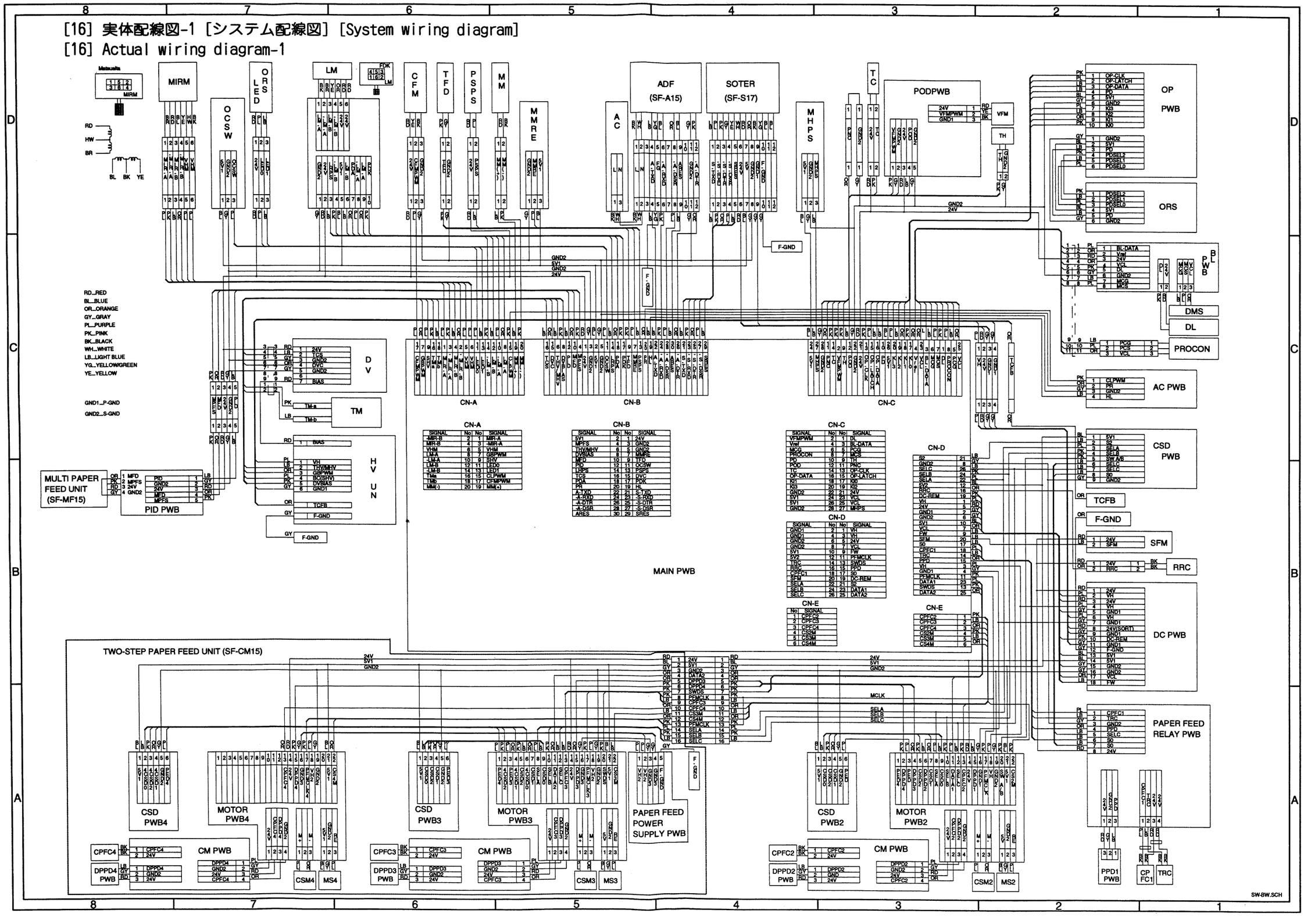
13. SF-2116/1016 本体トレイ A3給紙
13. SF-2116/1016 body tray paper feed A3



*1 レジスト量（シミュレーション51-2で設定）
*2 用紙先端位置（シミュレーション50-1で設定）

*1 Resist amount (Set with simulation 51-2.)
*2 Paper lead edge position (Set with simulation 50-1.)

[16] 実体配線図-1 [システム配線図] [System wiring diagram]
[16] Actual wiring diagram-1



CN-A

SIGNAL	No	No	SIGNAL
-MIR-B	2	1	MIR-A
MIR-B	4	3	-MIR-A
VHM	6	5	VHM
LM-A	8	7	GBPWM
-LM-A	10	9	SHV
LM-B	12	11	LED0
-LM-B	14	13	LED1
TMo	16	15	CLPWM
TMb	18	17	CFMP-WM
MM(-)	20	19	MM(+)

CN-B

SIGNAL	No	No	SIGNAL
5V1	2	1	24V
MPFS	4	3	GND2
THVMHV	6	5	GND2
DVBIAS	8	7	MMRE
MCD	10	9	TFD
PID	12	11	OCSW
LHPS	14	13	PSPS
TCS	16	15	DVC
PDA	18	17	PDK
PR	20	19	HL
A-TXD	22	21	S-TXD
-A-RXD	24	23	-S-RXD
-A-DTR	26	25	-S-DTR
-A-DSR	28	27	-S-DSR
ARES	30	29	SRES

CN-C

SIGNAL	No	No	SIGNAL
VFMPWM	2	1	DL
Vref	4	3	BL-DATA
MCG	6	5	PCS
PROCON	8	7	MCS
PD	10	9	ITC
POD	12	11	PNC
Tc	14	13	OP-CLK
OP-DATA	16	15	OP-LATCH
OH	18	17	K00
K03	20	19	K02
GND2	22	21	24V
5V1	24	23	VCL
5V1	26	25	VCL
GND2	28	27	MHPS

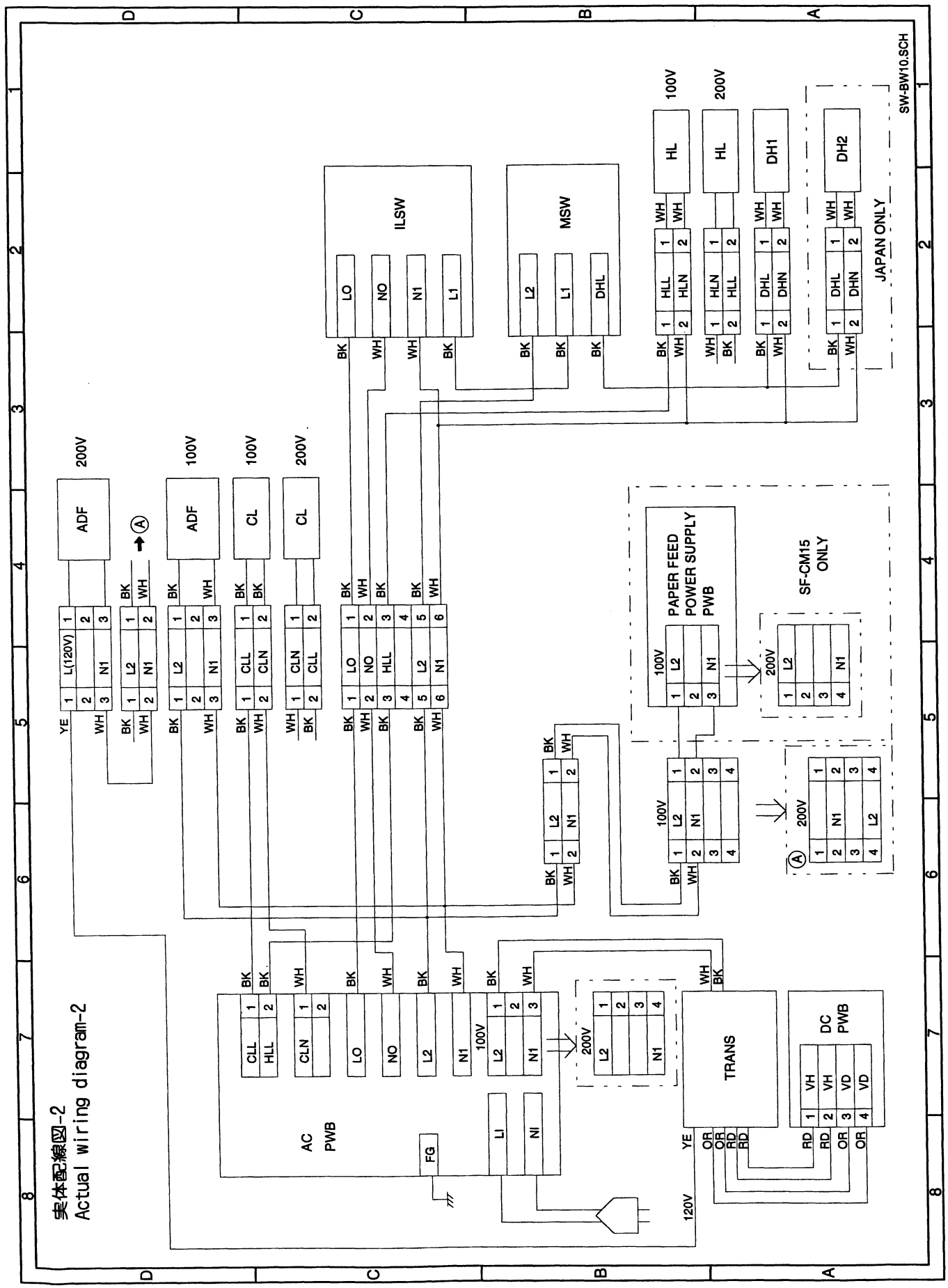
CN-D

SIGNAL	No	No	SIGNAL
GND1	2	1	VH
GND1	4	3	VH
GND2	6	5	24V
GND2	8	7	VCL
5V1	10	9	FW
5V2	12	11	PFMCLK
TRC	14	13	SWDS
RRC	16	15	PPD
CPFC1	18	17	SO
SFM	20	19	DC-REM
SELA	22	21	S2
SELB	24	23	DATA1
SELC	26	25	DATA2

CN-E

No	SIGNAL
1	CPFC2
2	CPFC3
3	CPFC4
4	CSSM
5	CSSM
6	CSSM

実体配線図-2
Actual wiring diagram-2



実体配線図-3（ヒーター付きモデル：日本国内専用）